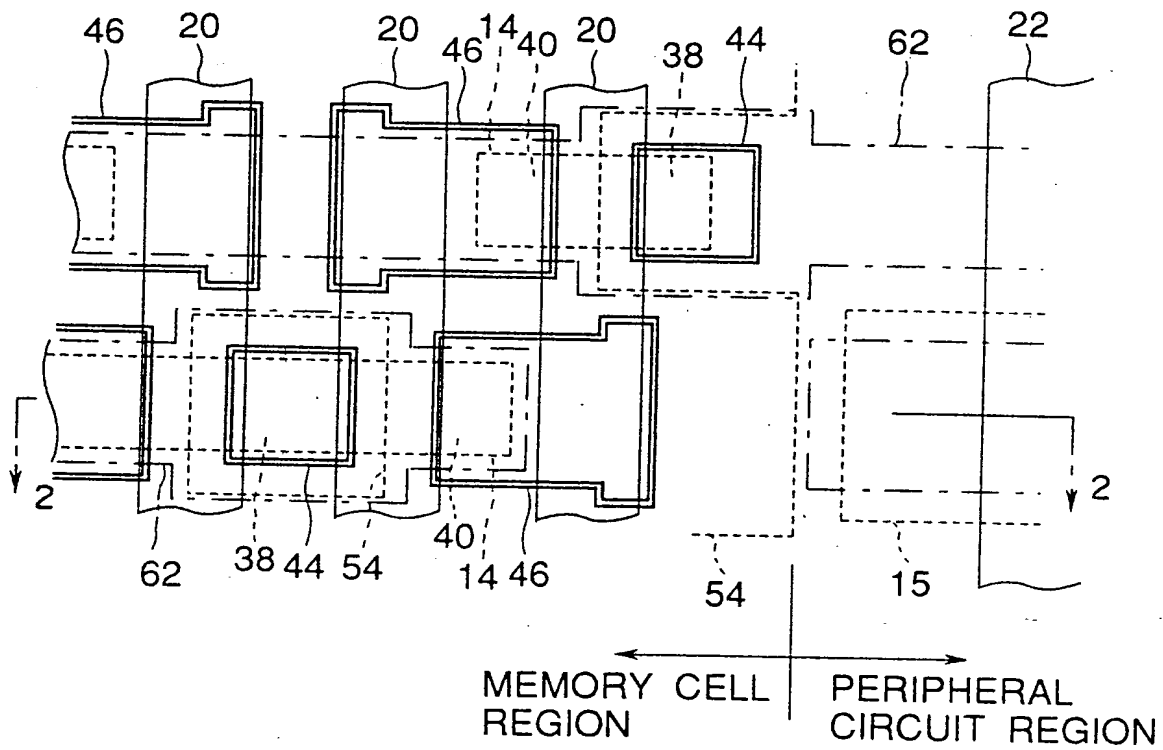
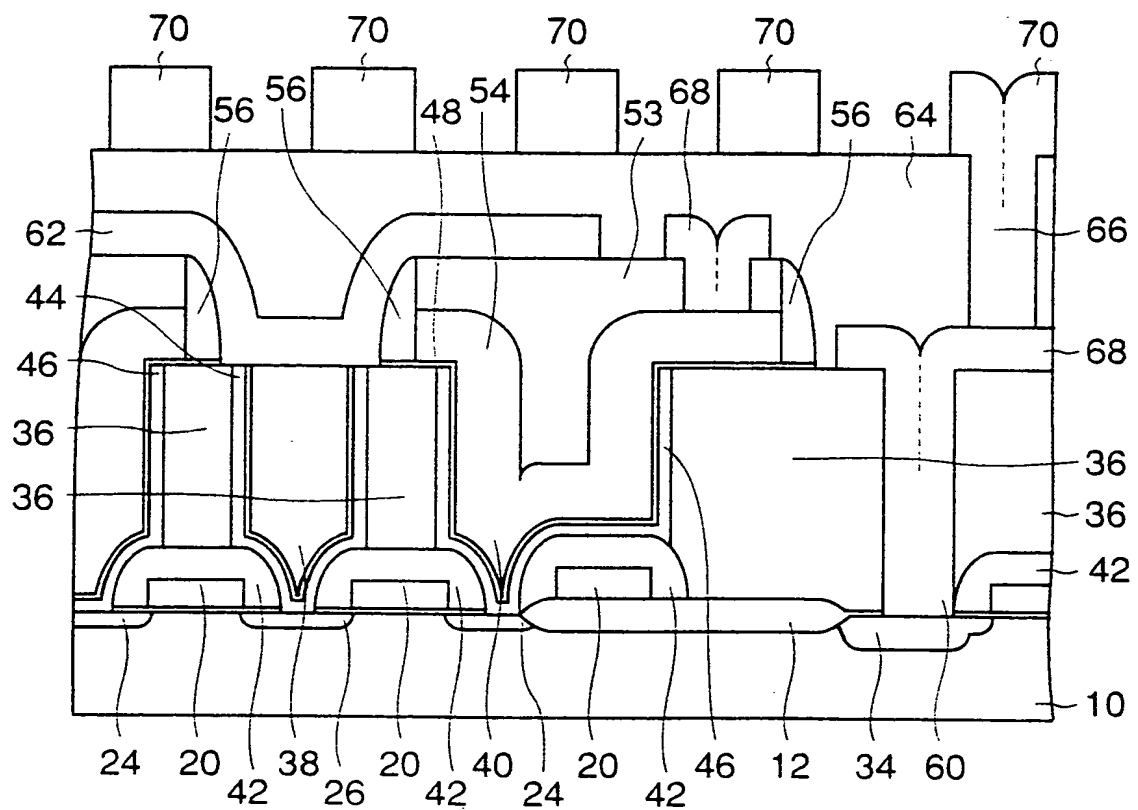


FIG.1





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FIG.3A

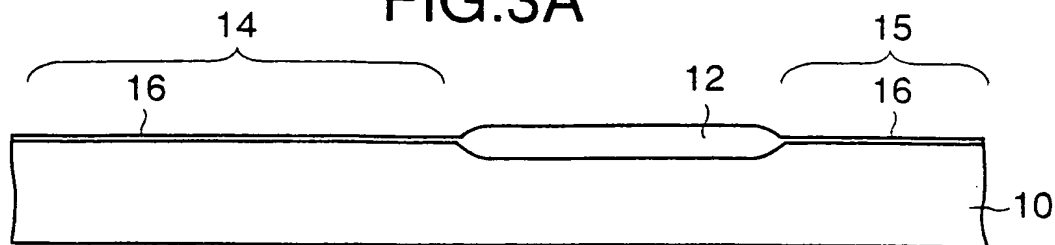


FIG.3B

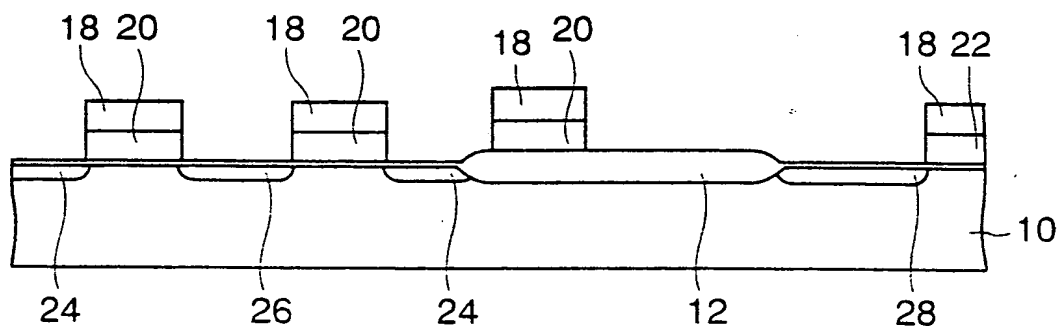


FIG.3C

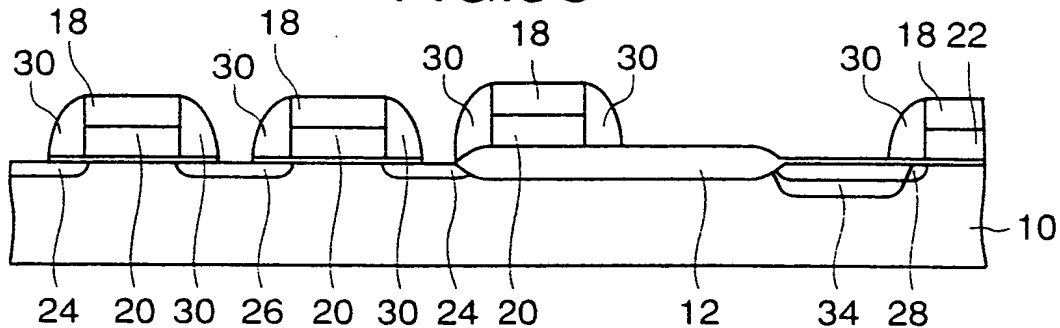


FIG.3D

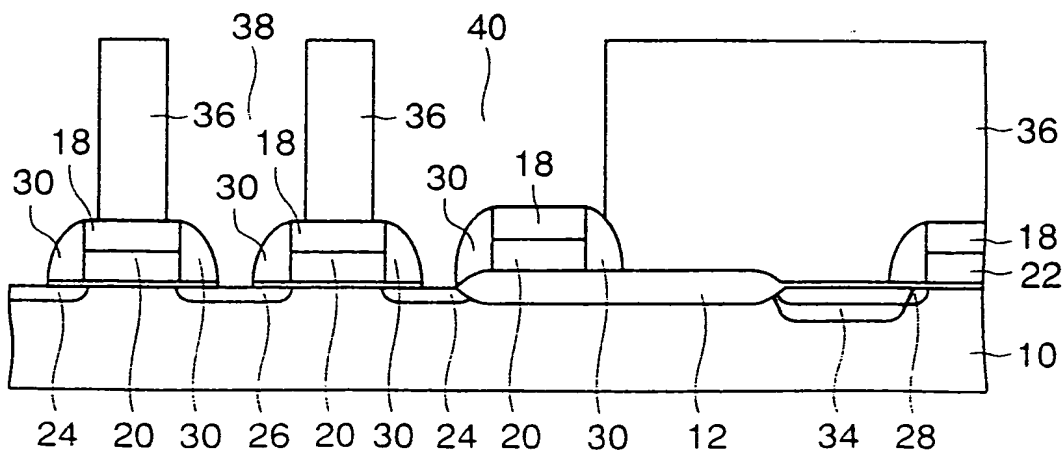


FIG.4A

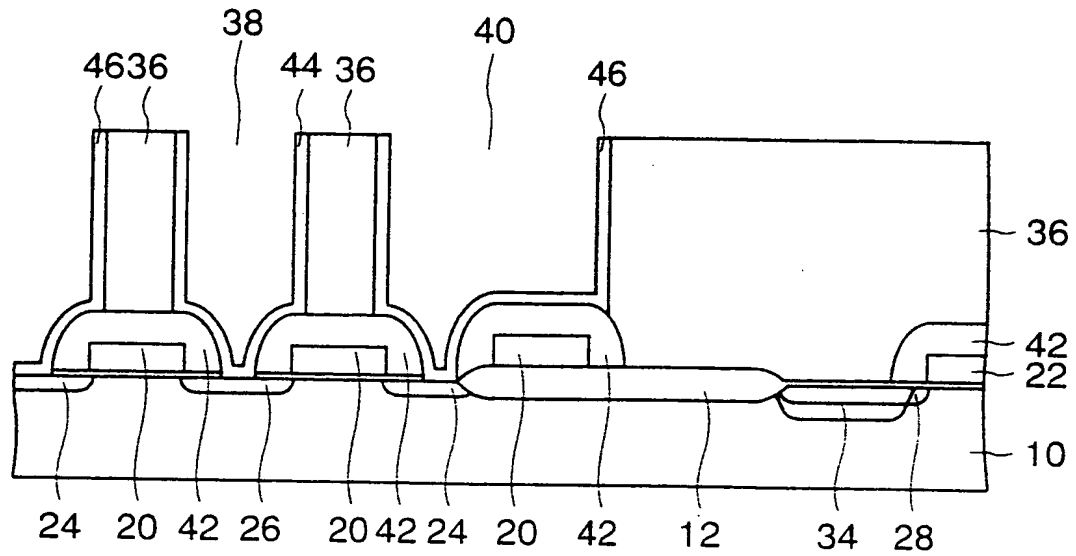
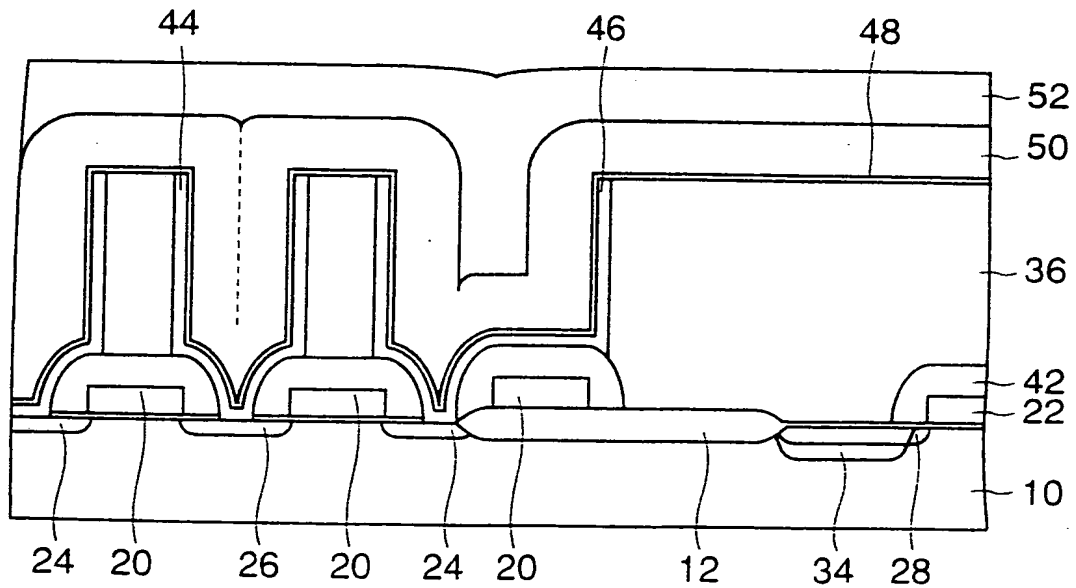


FIG.4B



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FIG.5A

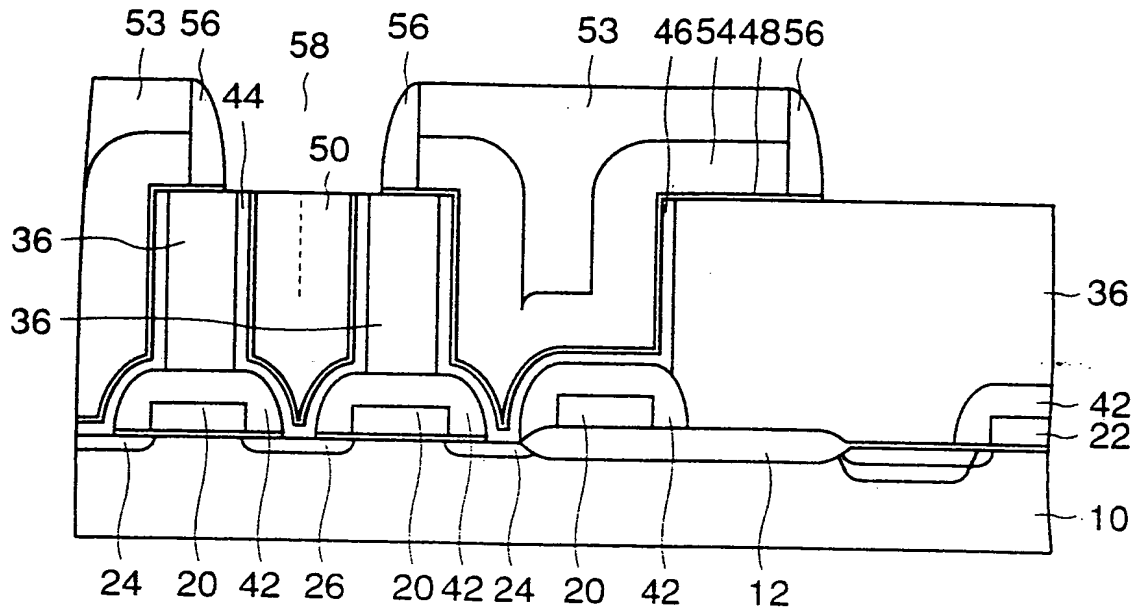


FIG.5B

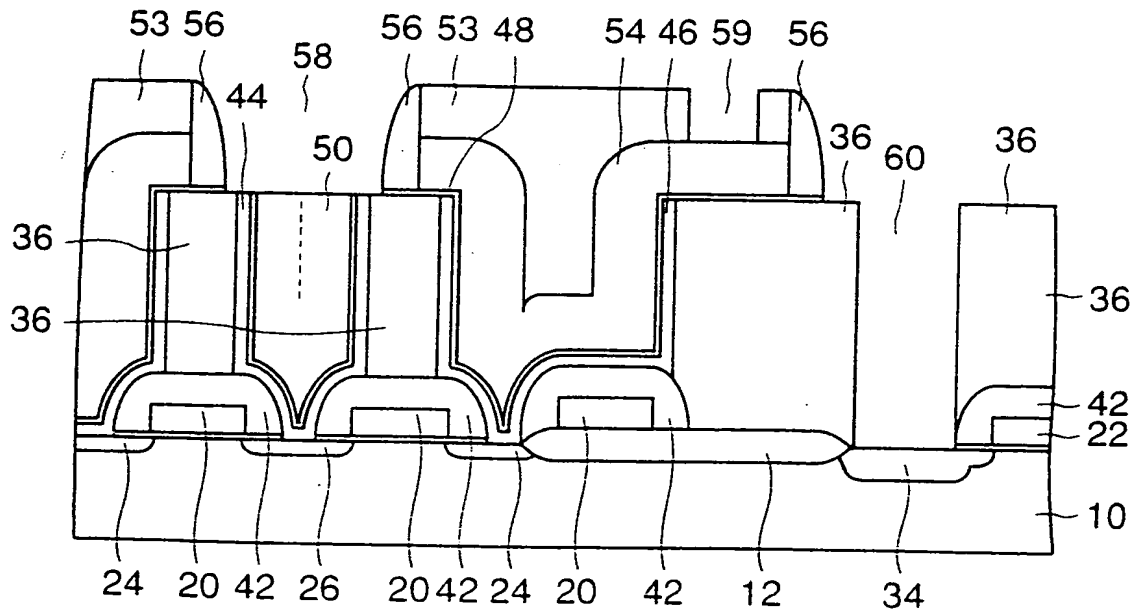


FIG.6

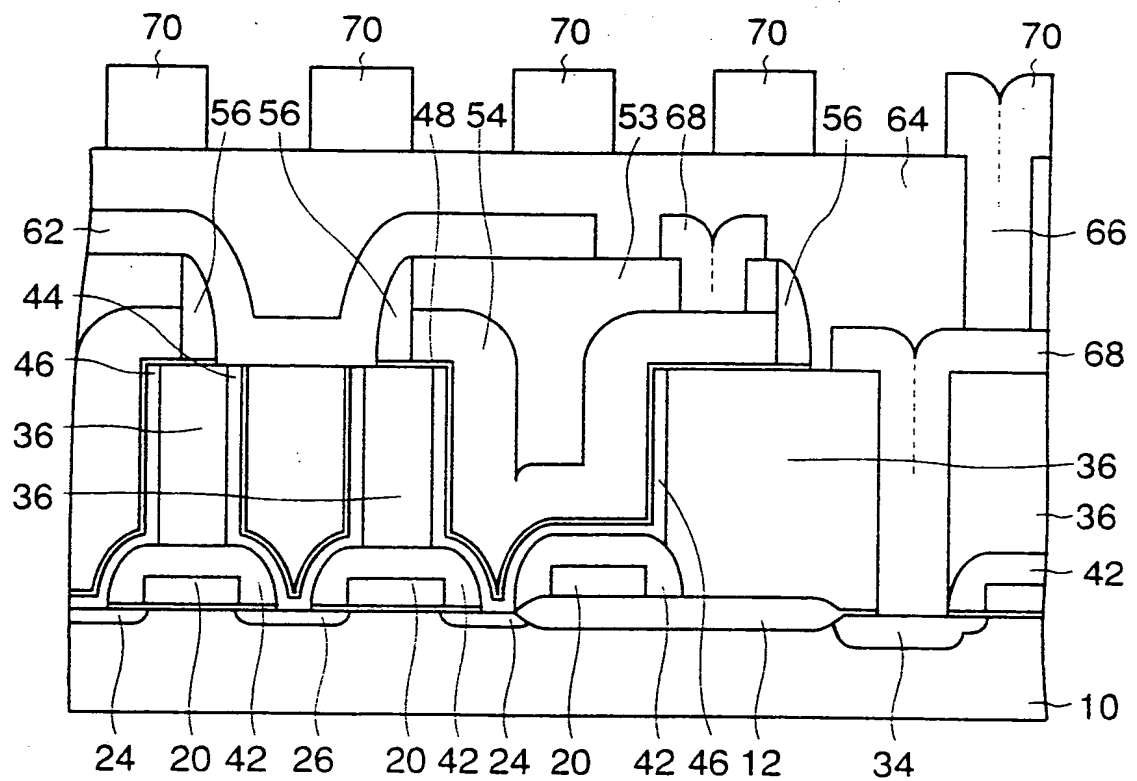
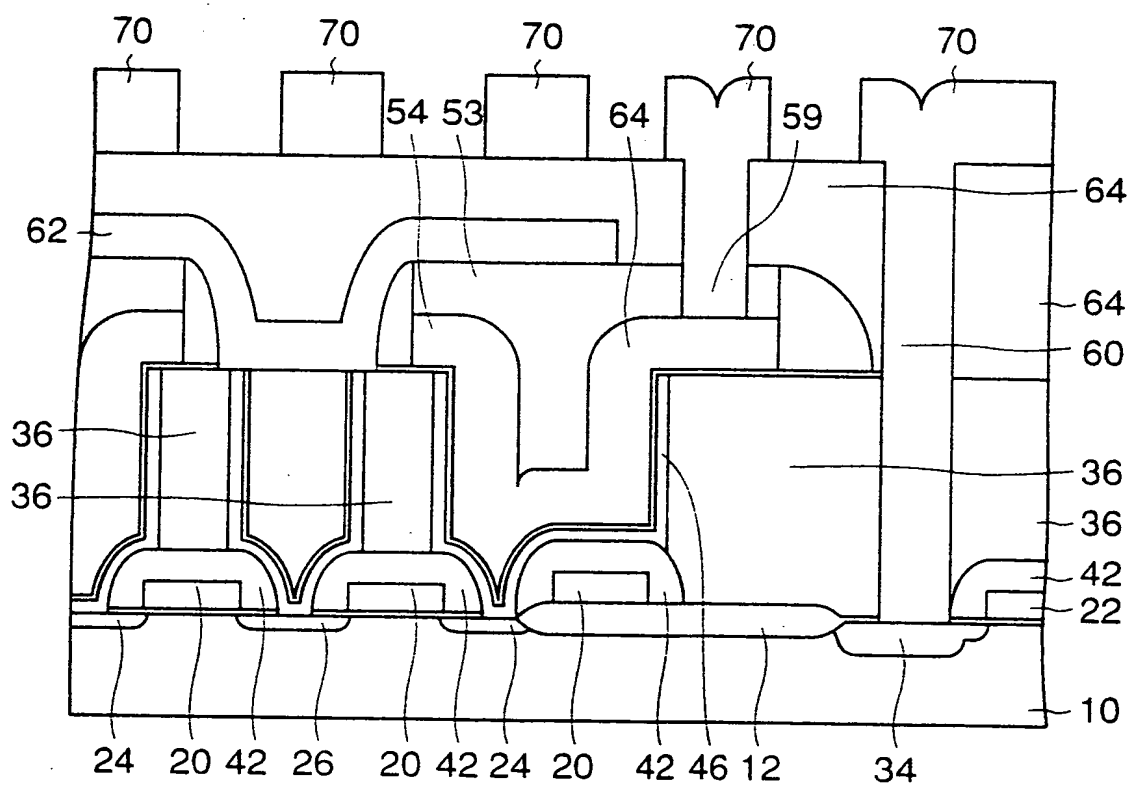


FIG.7



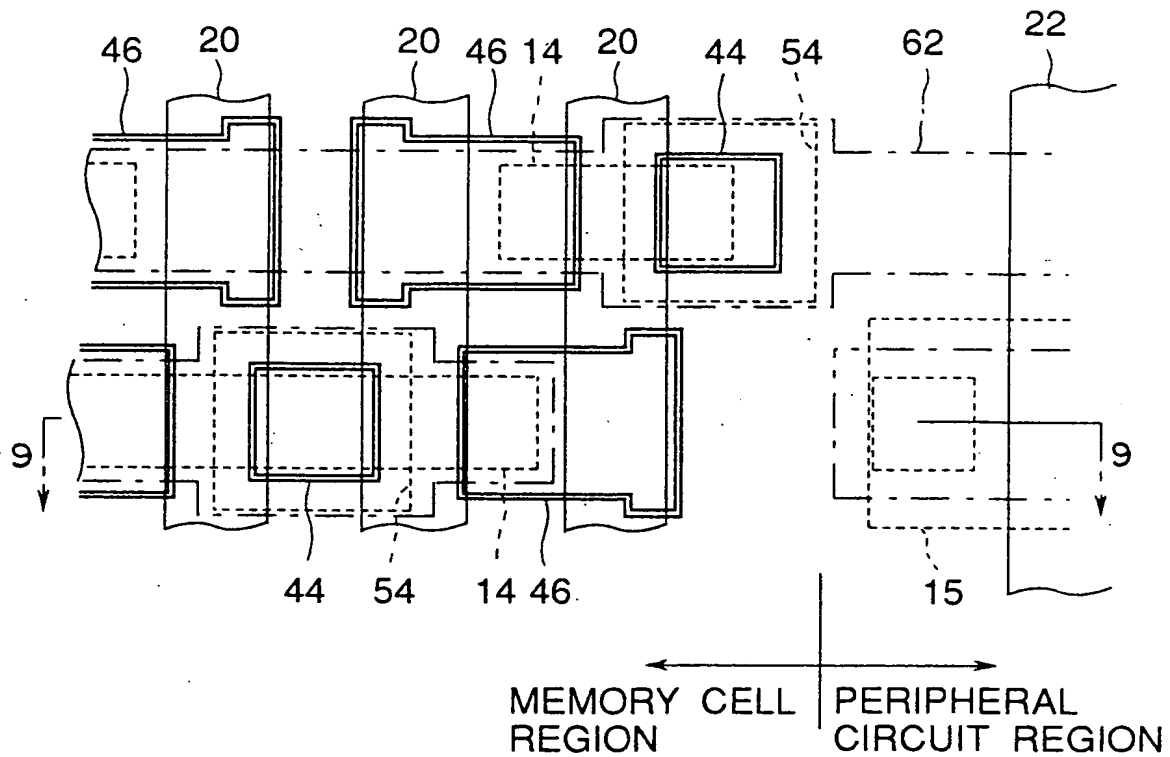
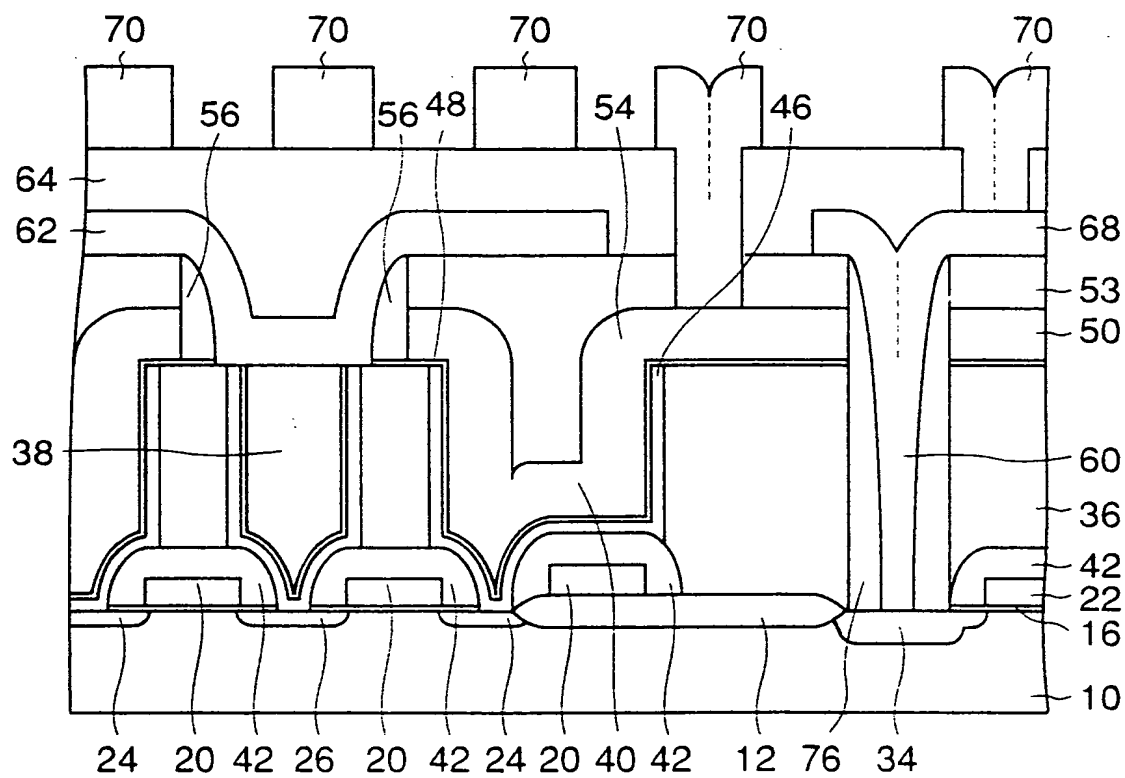




FIG.9



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FIG.10A

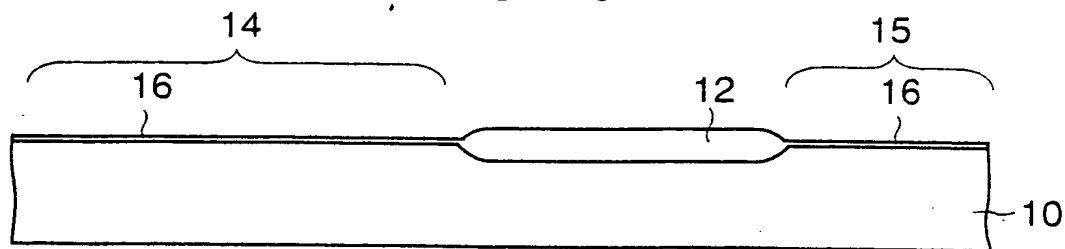


FIG.10B

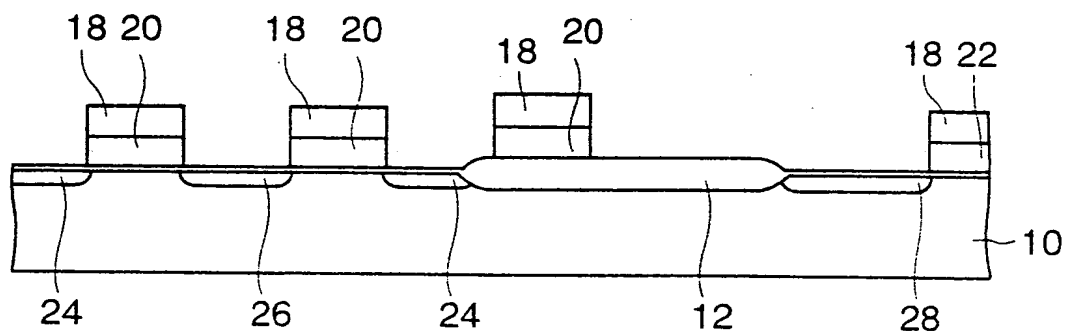


FIG.10C

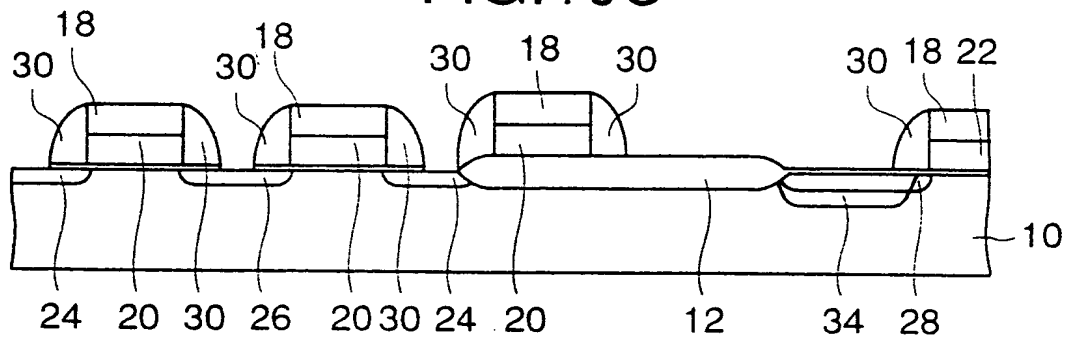
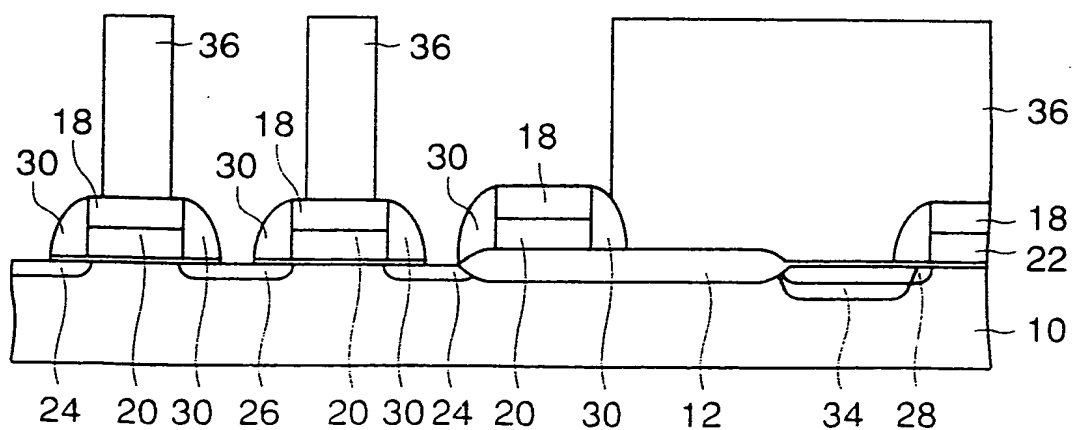


FIG.10D



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FIG.11A

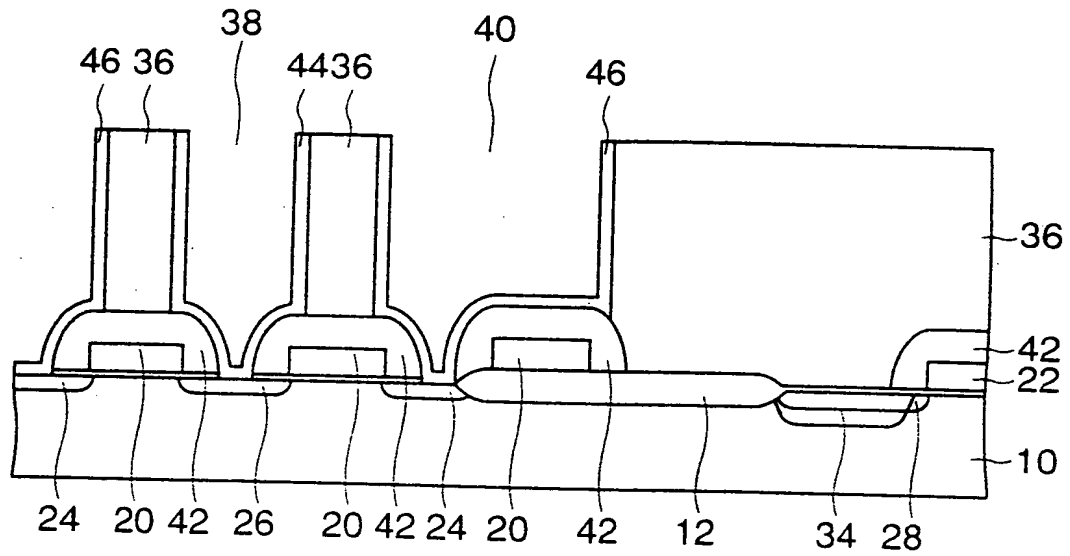
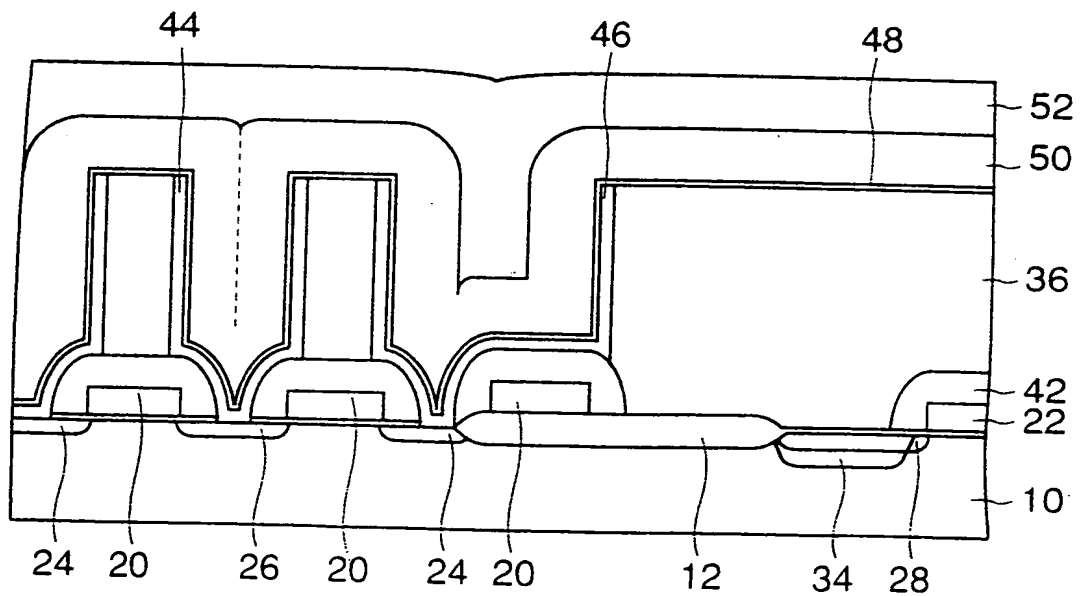


FIG.11B



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FIG.12A

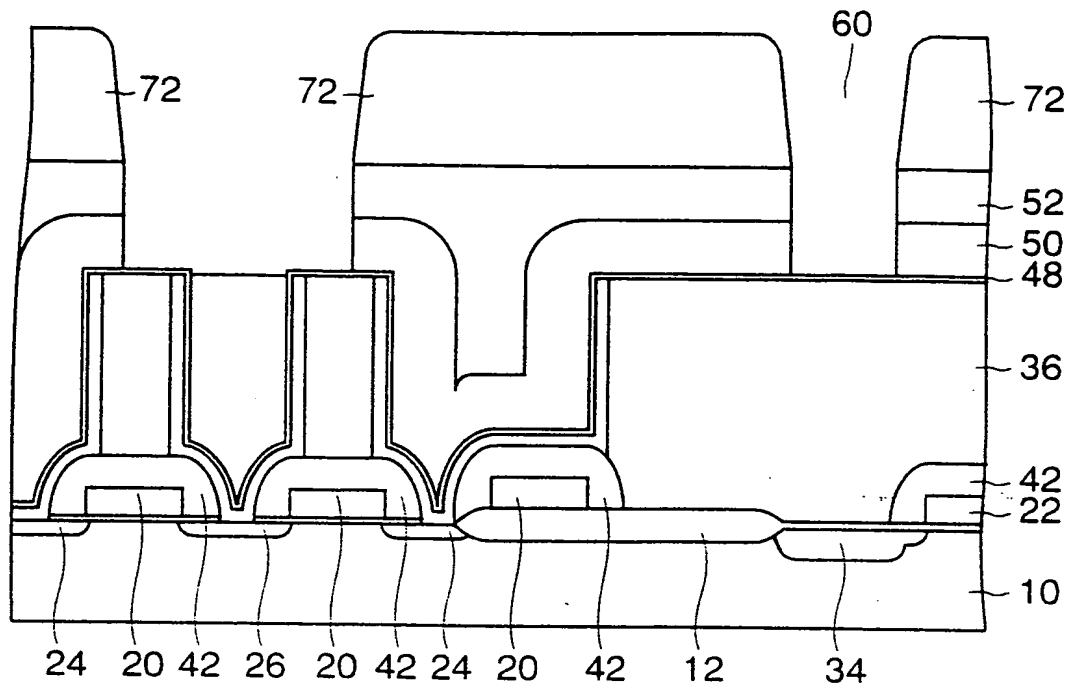
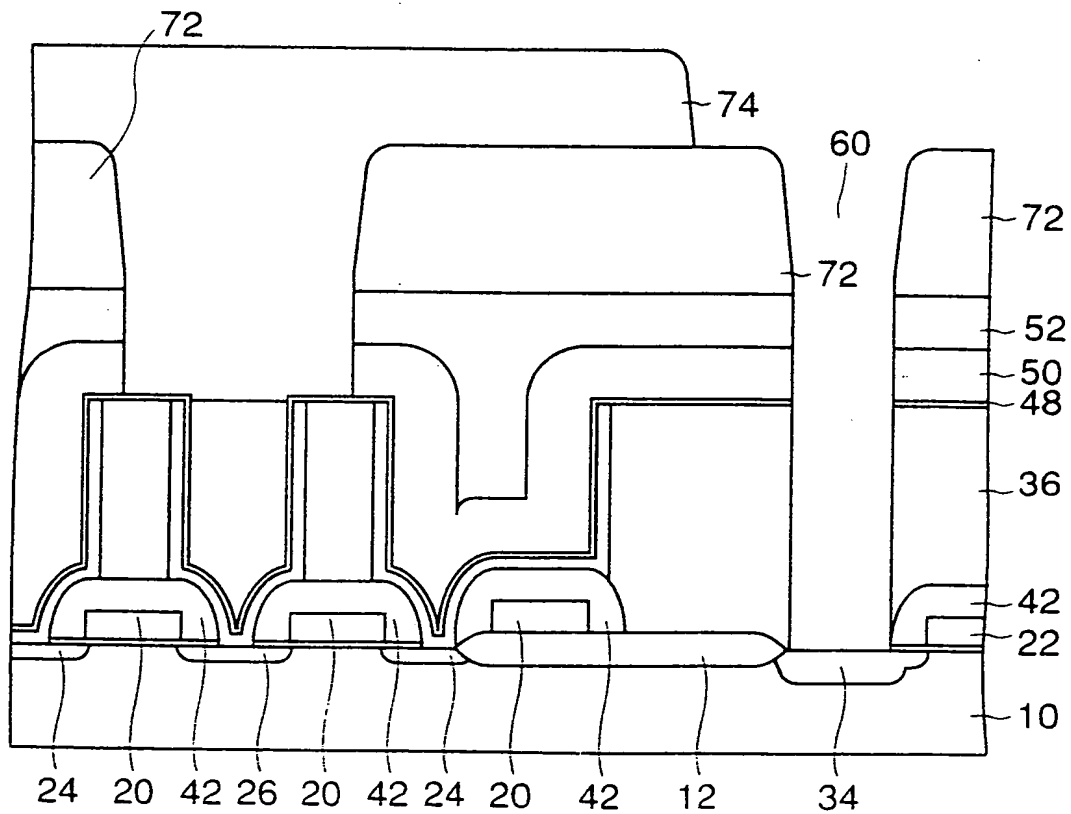


FIG.12B



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FIG.13A

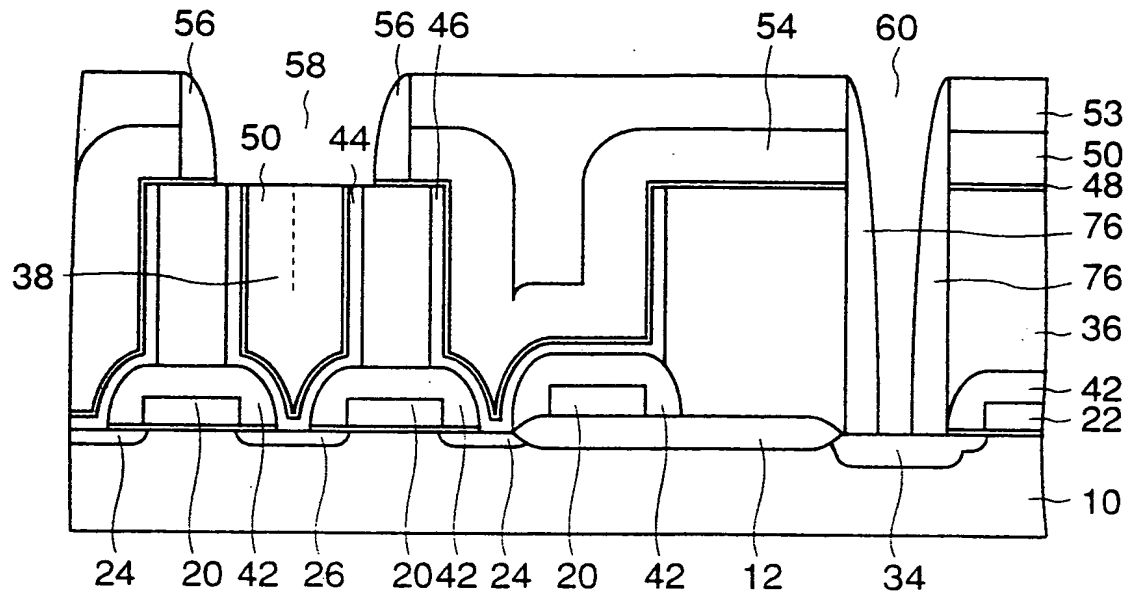
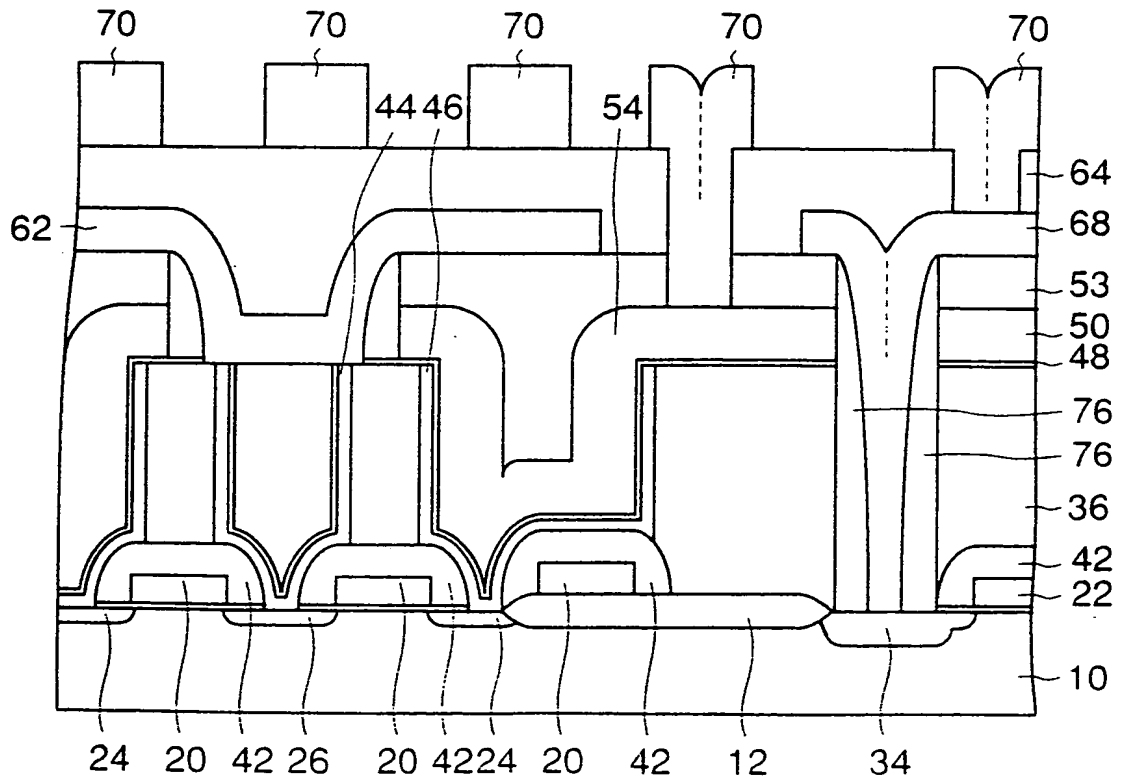
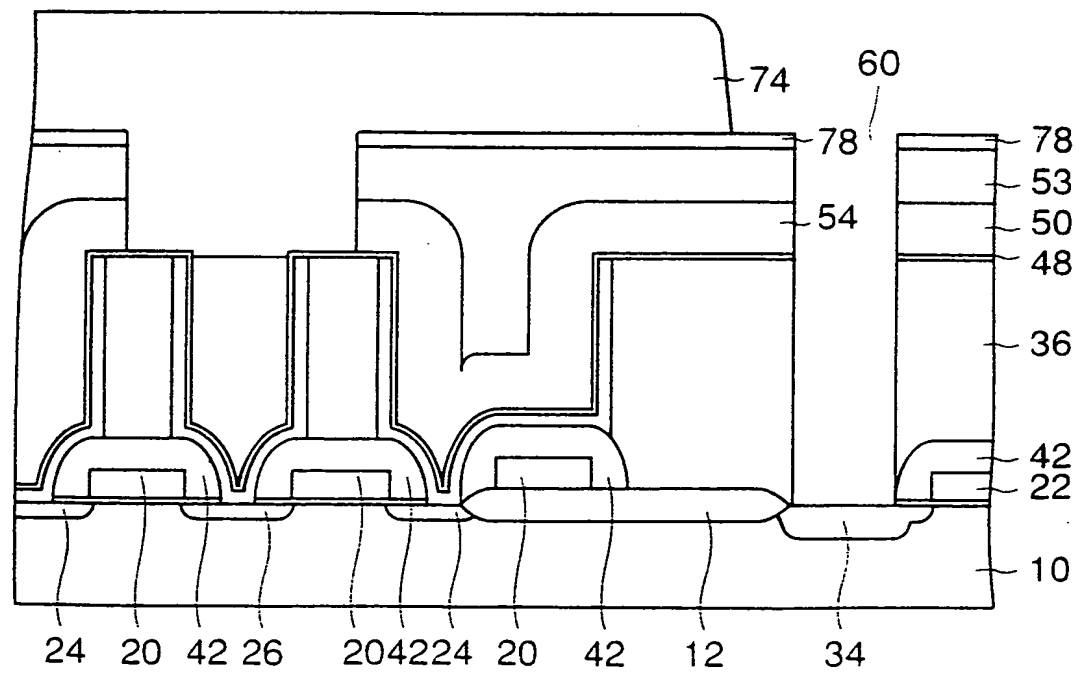


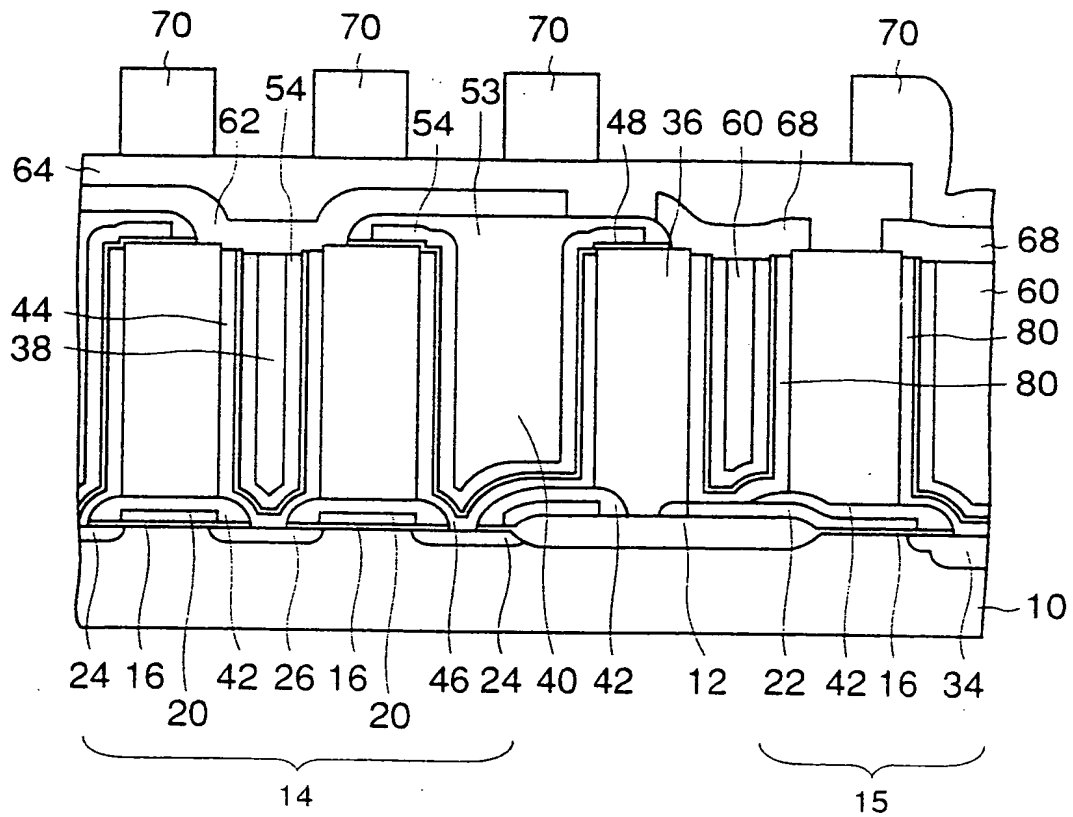
FIG.13B





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FIG.15



A cross-sectional view of a semiconductor device. The device consists of a substrate 10 with a top surface 12. A series of features are formed on the top surface 12. From left to right, these include: a first feature with a top layer 20 and a bottom layer 18, situated on a base 24; a second feature with a top layer 20 and a bottom layer 18, situated on a base 26; a third feature with a top layer 16 and a bottom layer 20, situated on a base 24; a fourth feature with a top layer 82 and a bottom layer 22, situated on a base 12; and a fifth feature with a top layer 22 and a bottom layer 18, situated on a base 28. The device is shown in a cross-section with a vertical line 16 on the right side.

A cross-sectional view of a multi-layered structure 10. The structure consists of several layers: a top layer 30, a middle layer 20, and a bottom layer 12. The top layer 30 contains several rectangular features 18, which are separated by a layer 20. The middle layer 20 contains a central feature 82. The bottom layer 12 contains a central feature 22. The structure is shown in a cross-section, with various layers and features labeled with reference numerals: 30, 20, 18, 82, 22, 12, 24, 26, 28, and 34.

[illegible]



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FIG.17A

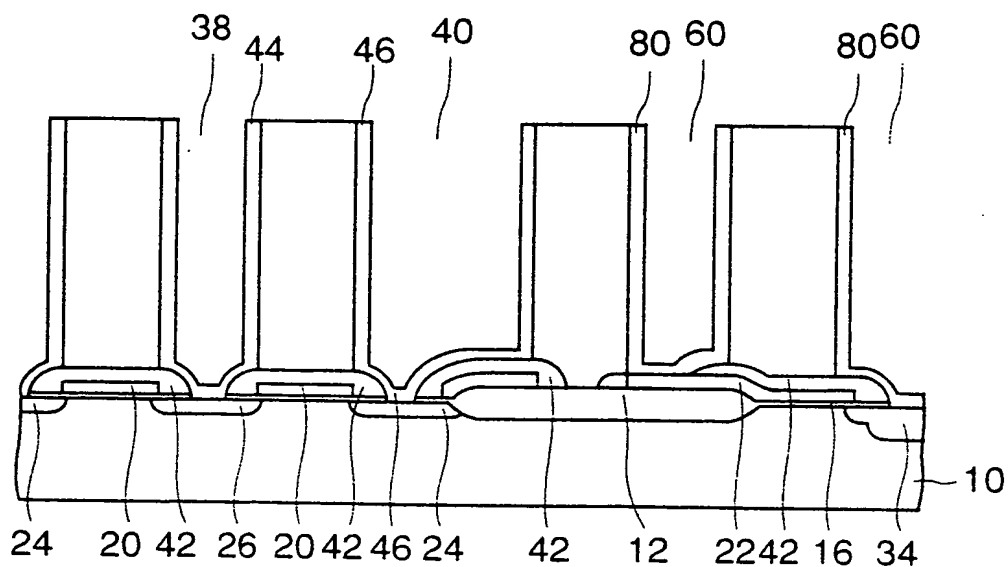
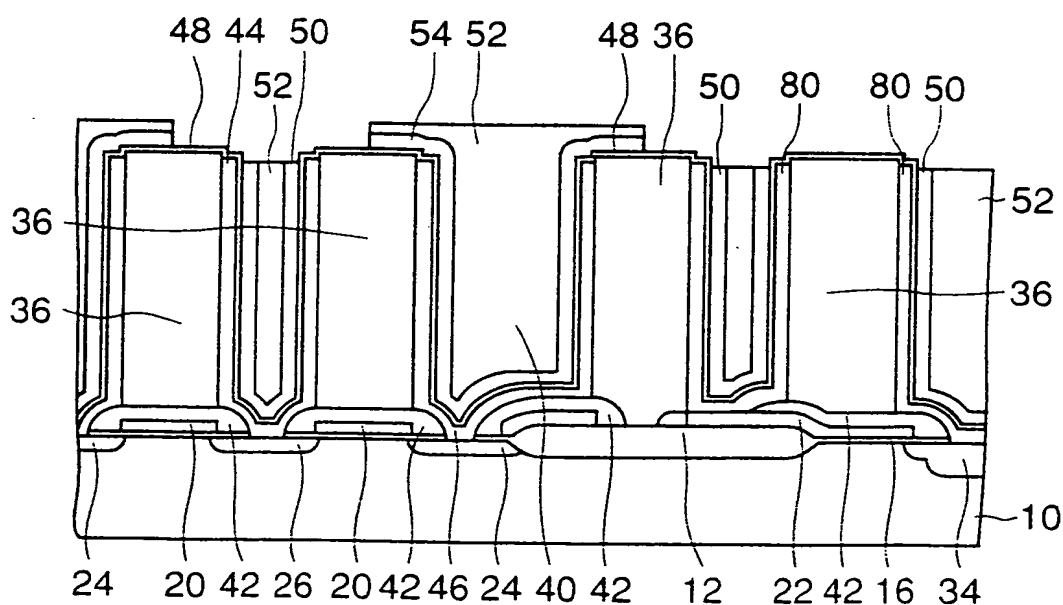


FIG.17B



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FIG.18A

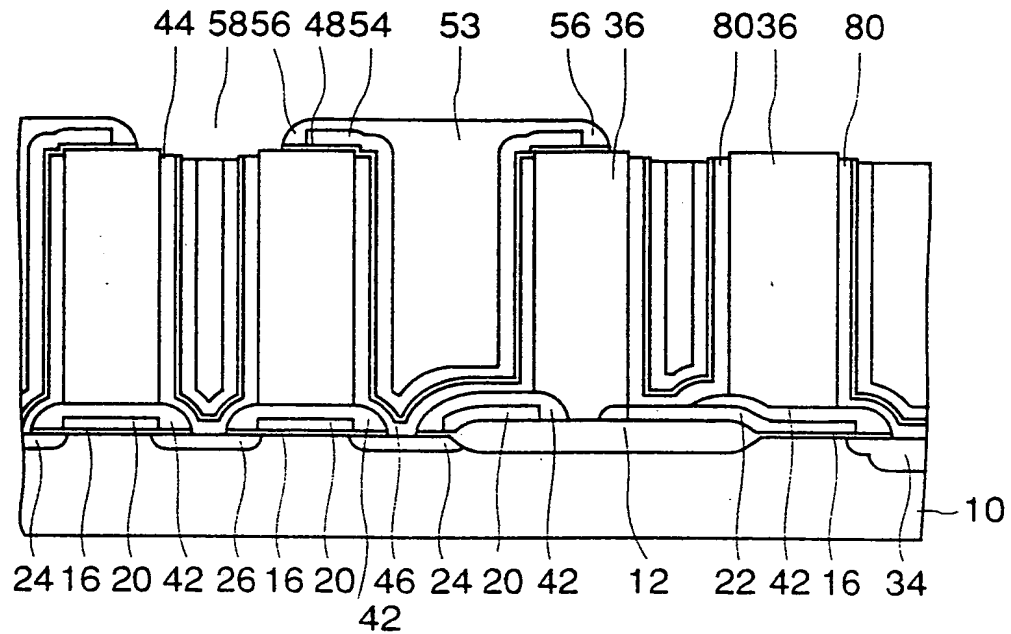
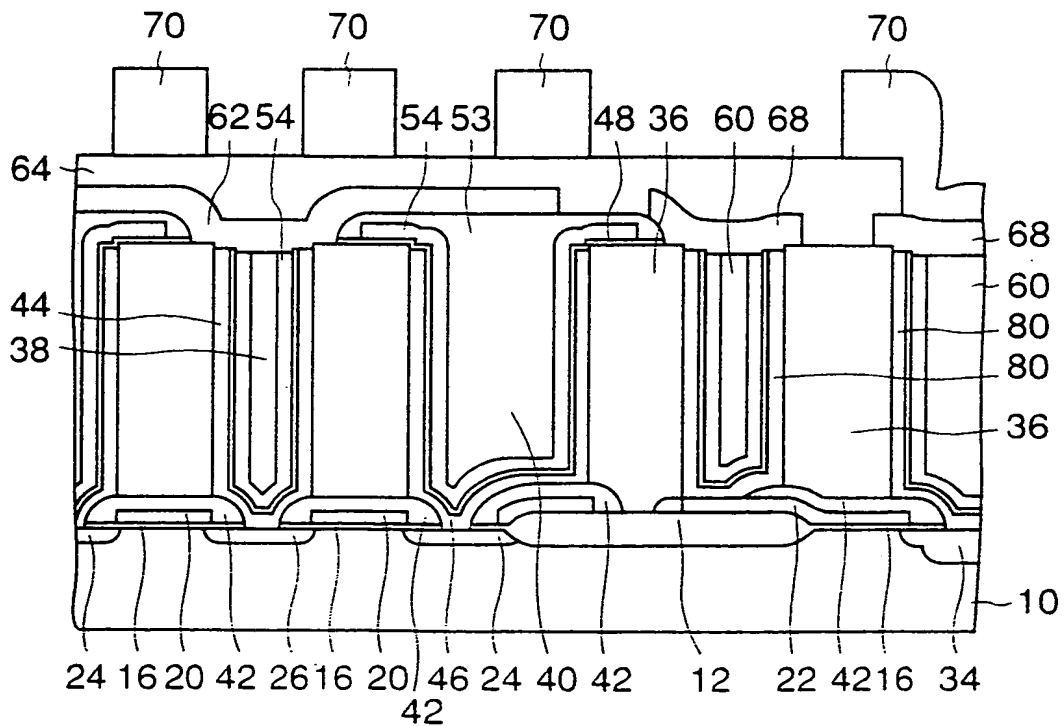
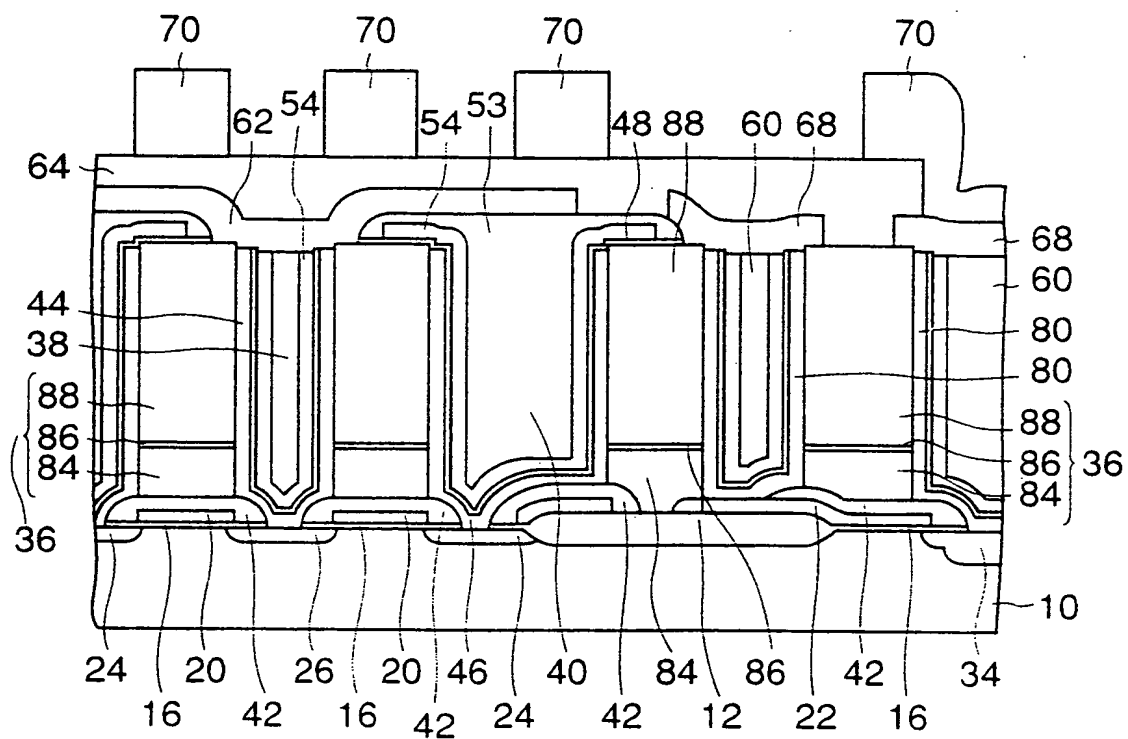


FIG.18B



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FIG.19



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FIG.20A

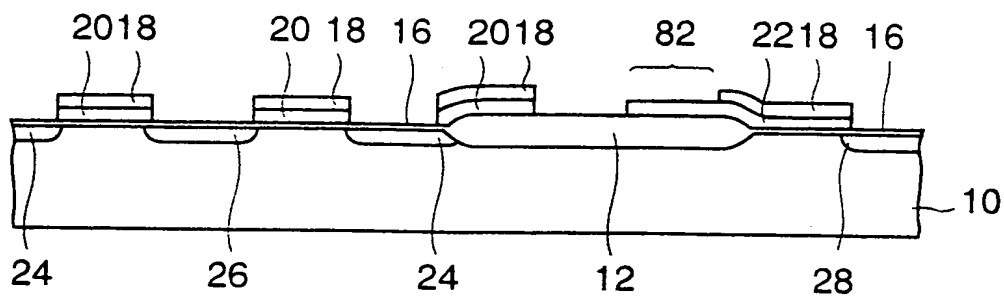


FIG.20B

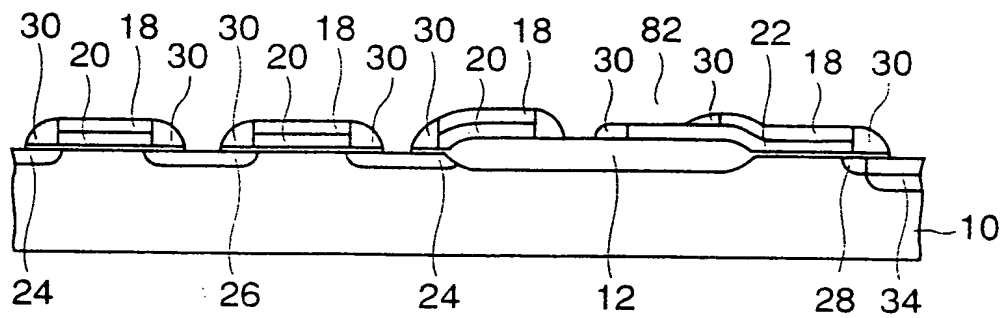
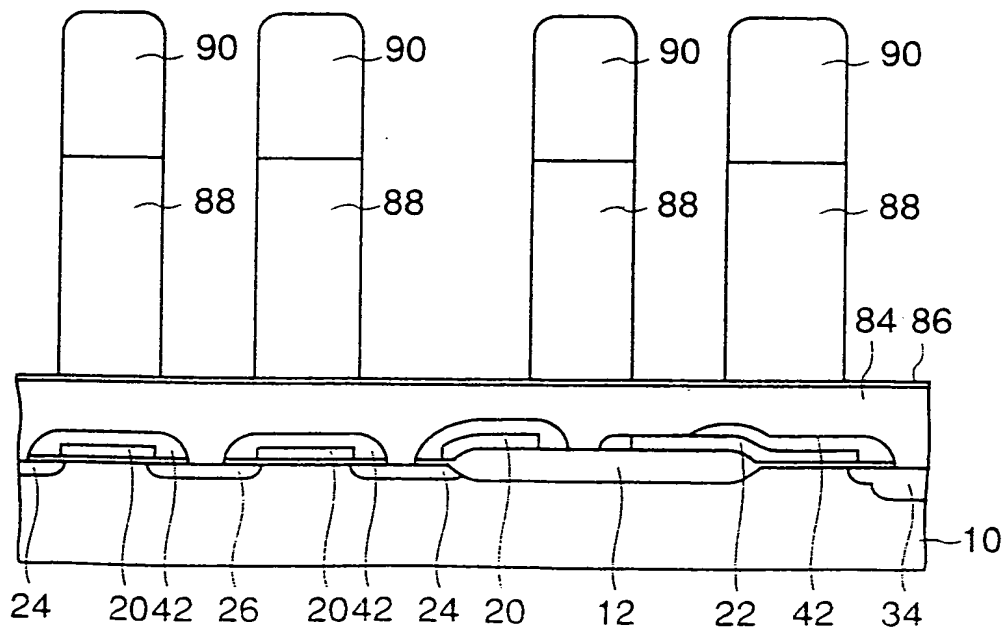


FIG.20C



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FIG.21A

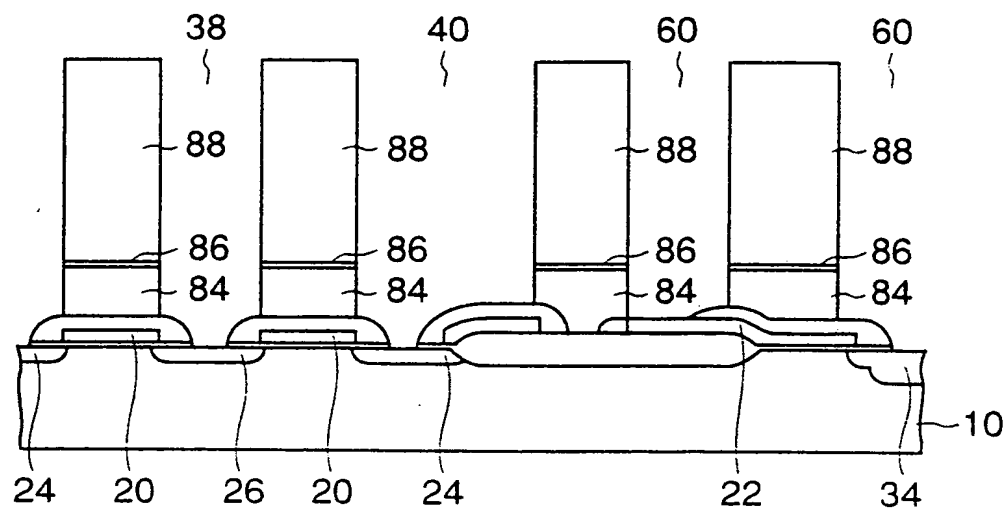
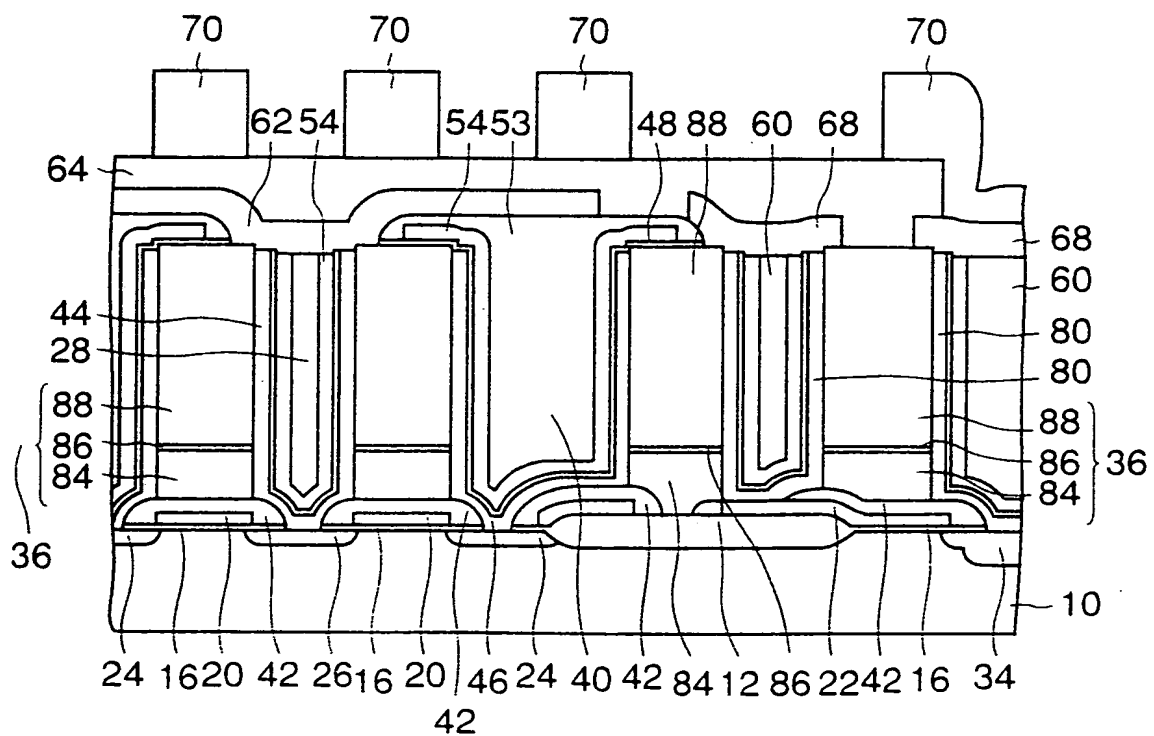
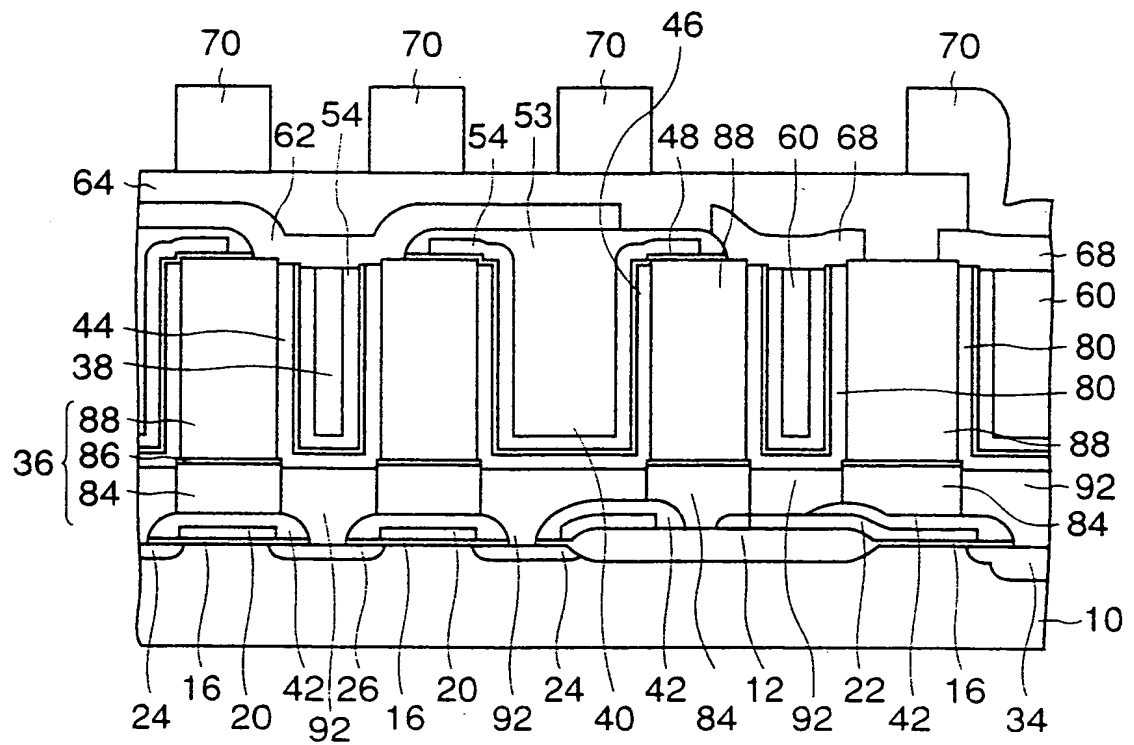


FIG.21B



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FIG.22



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FIG.23A

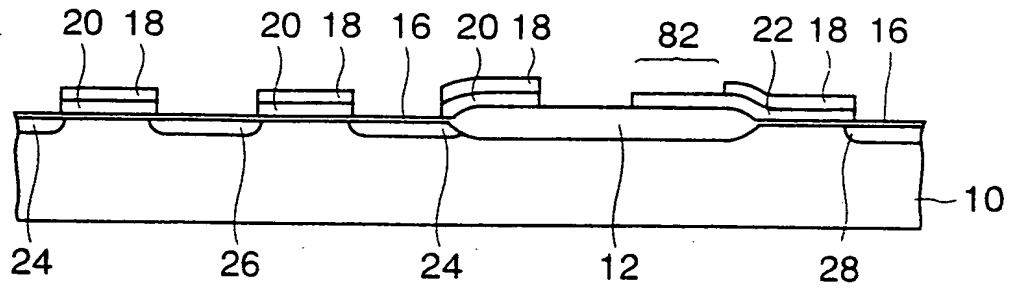


FIG.23B

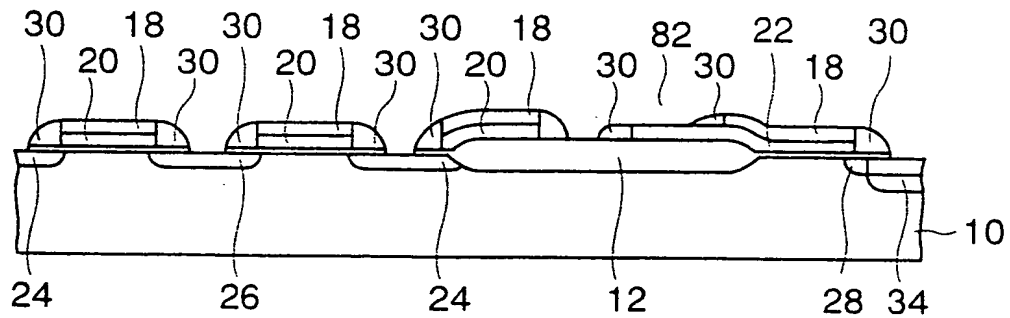
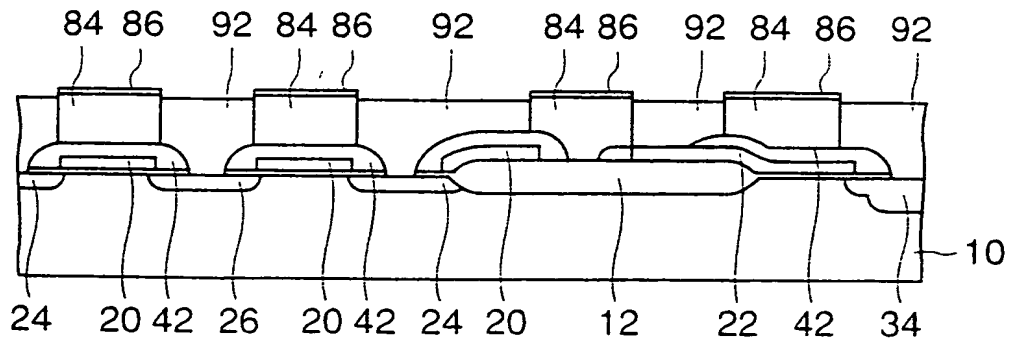


FIG.23C



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FIG.24A

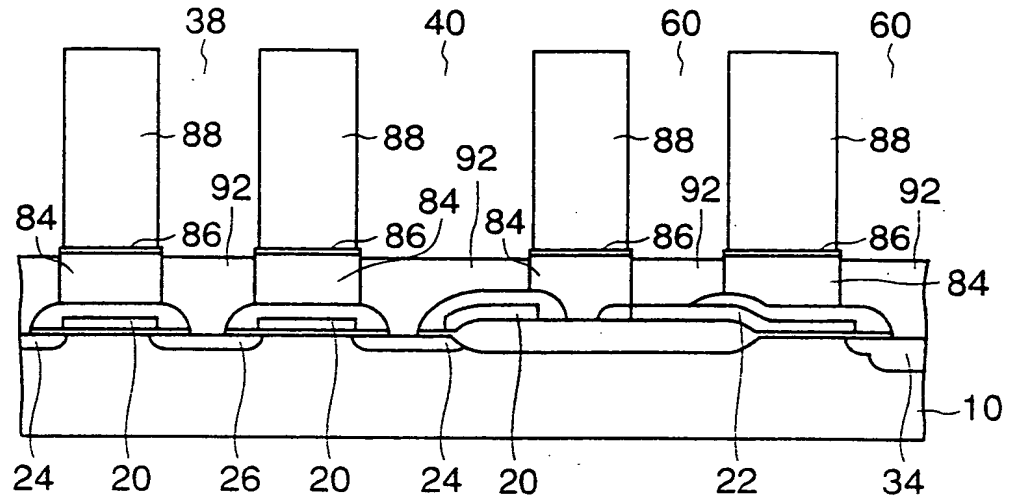
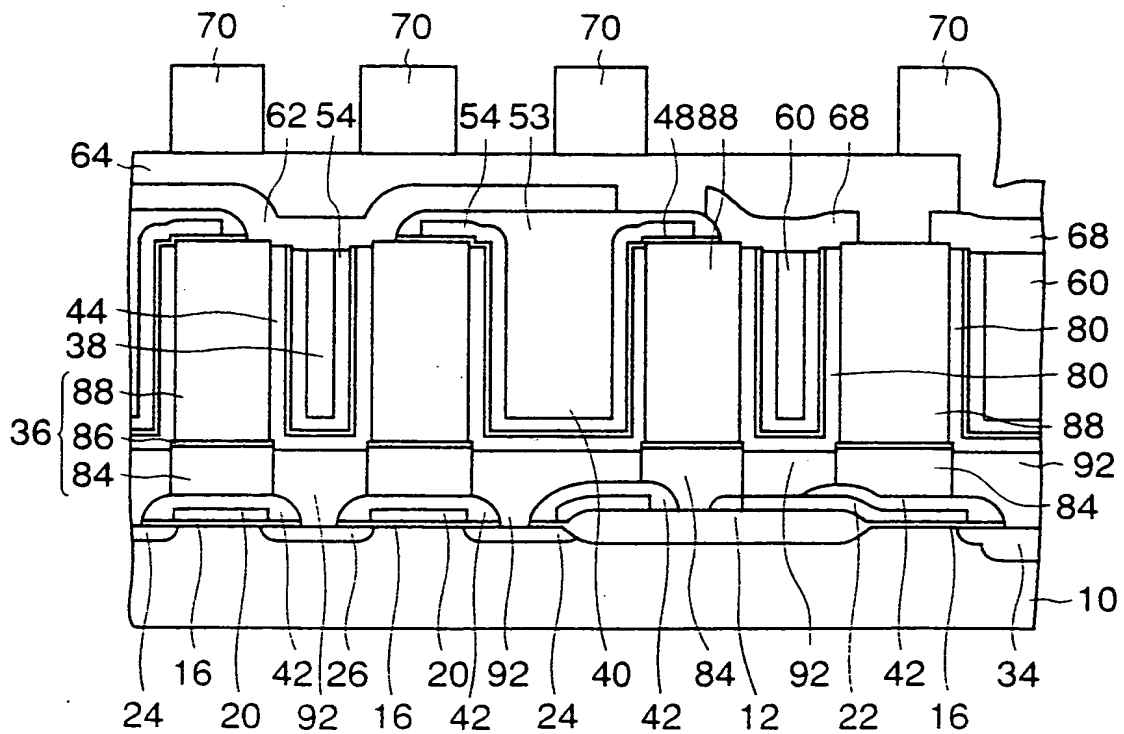


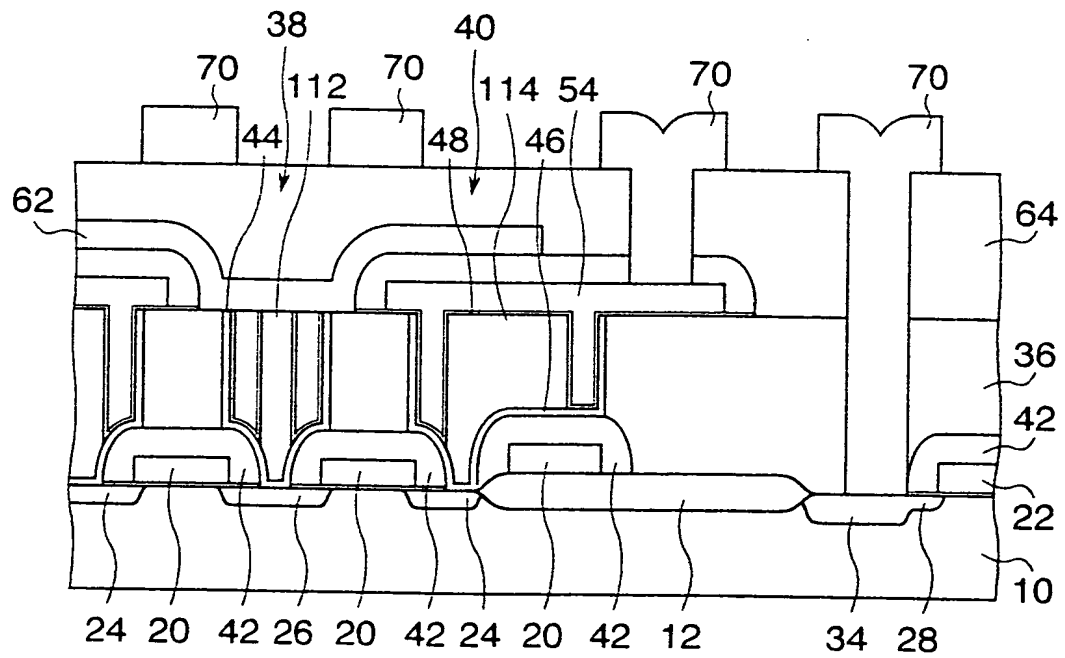
FIG.24B





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FIG.25



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FIG.26A

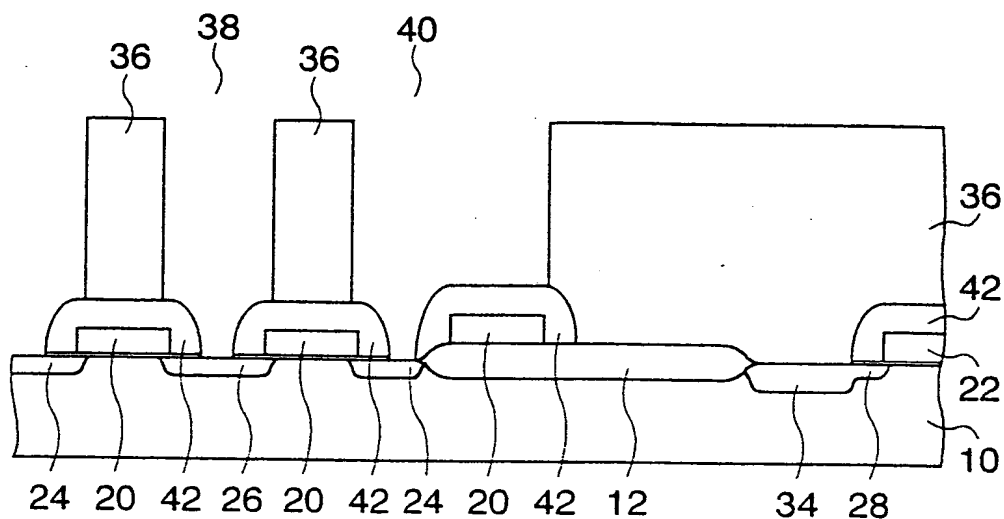
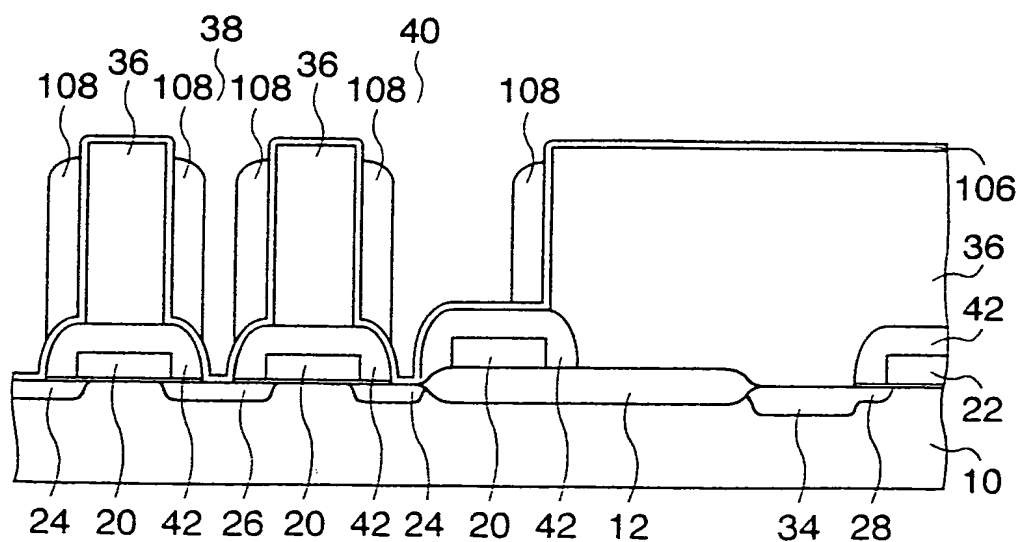


FIG.26B



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FIG.27A

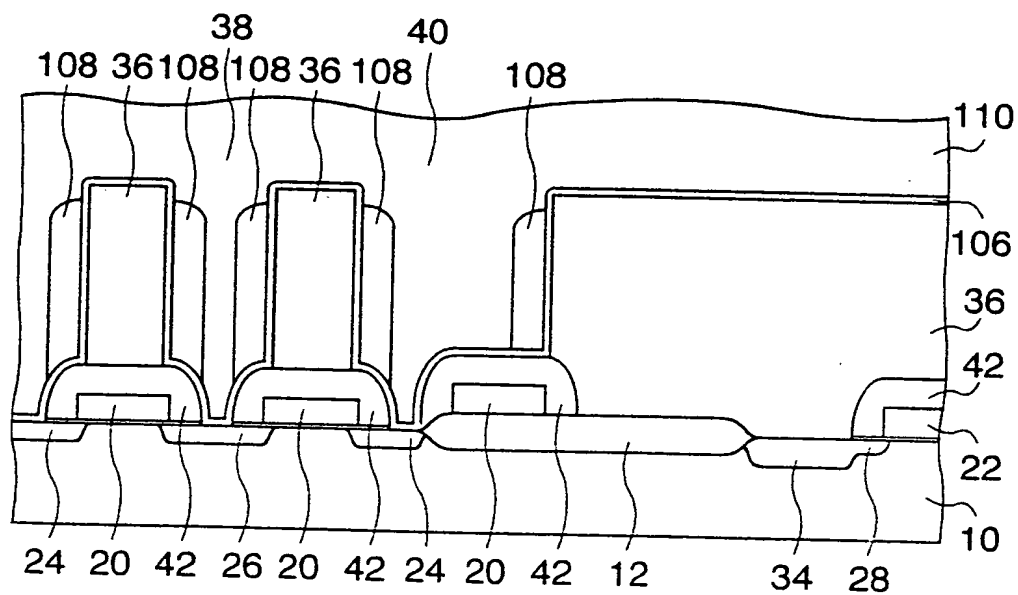
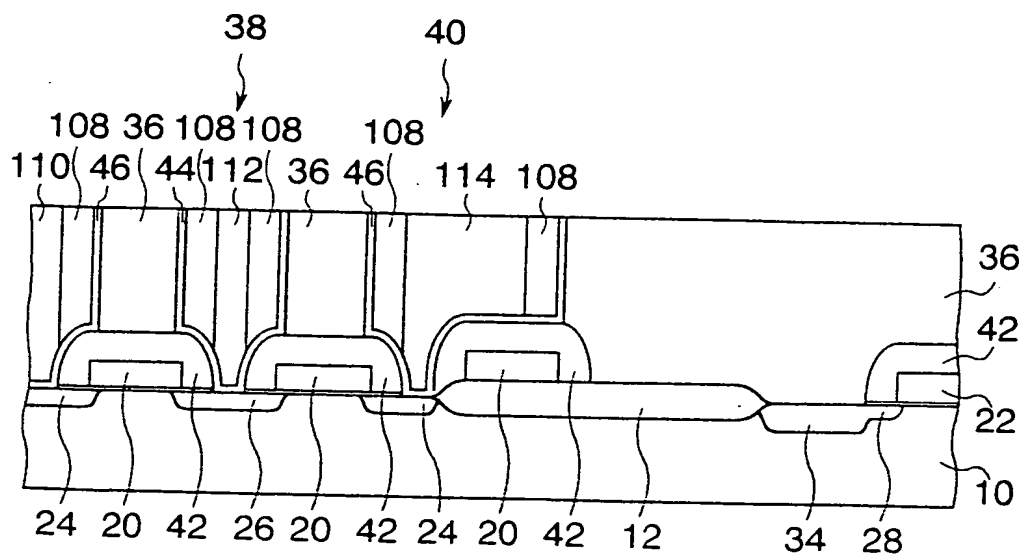


FIG.27B



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FIG.28A

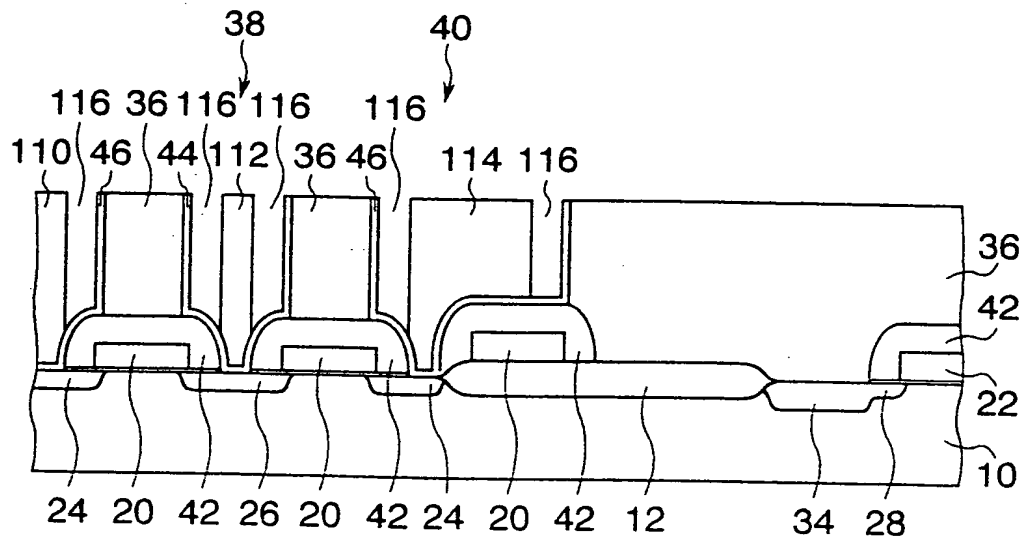


FIG.28B

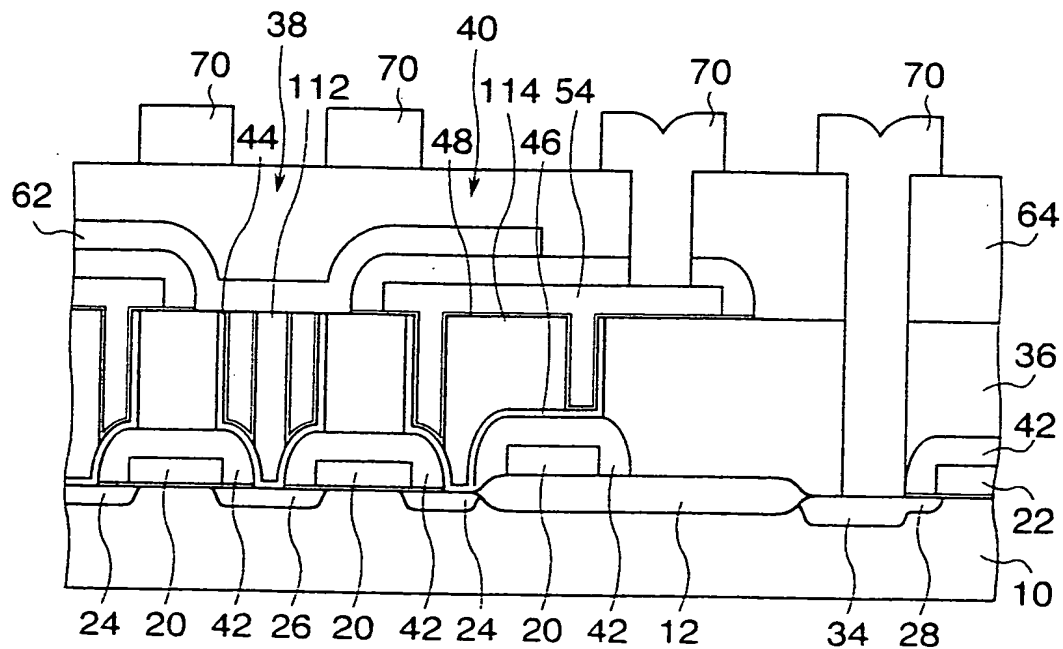
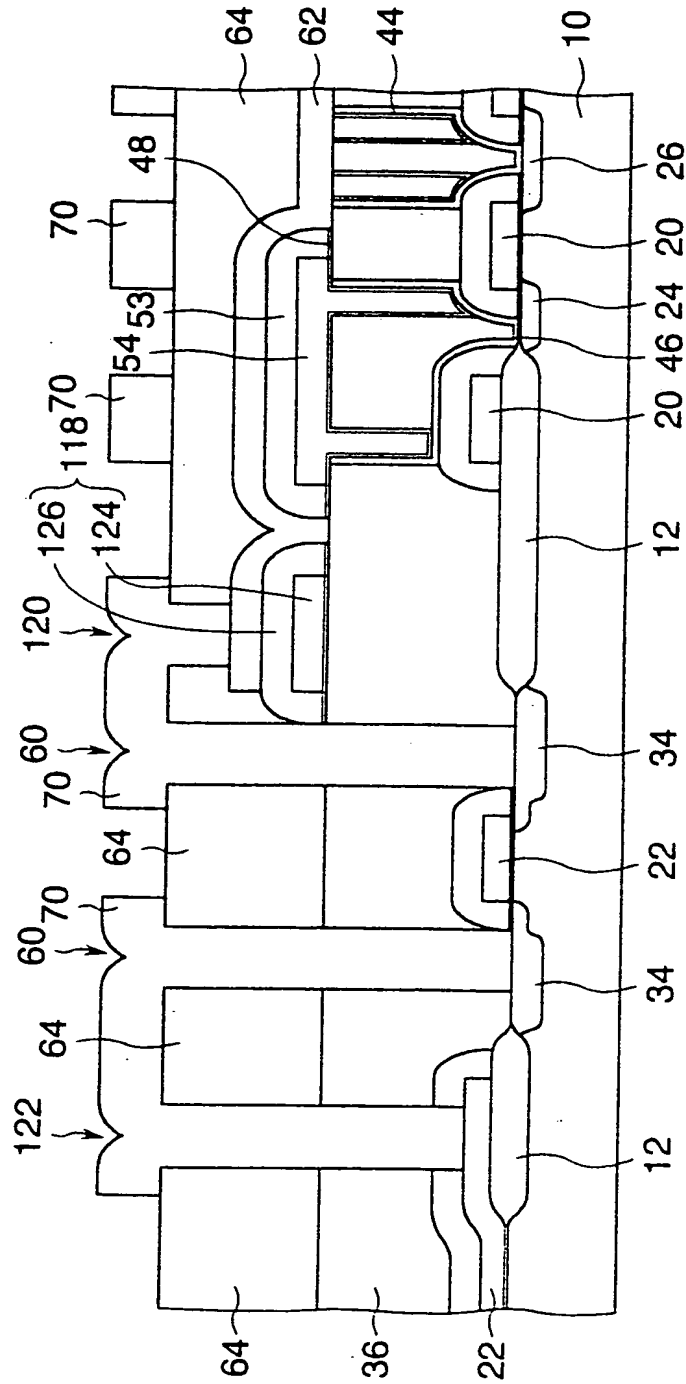


FIG. 29



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FIG.30A

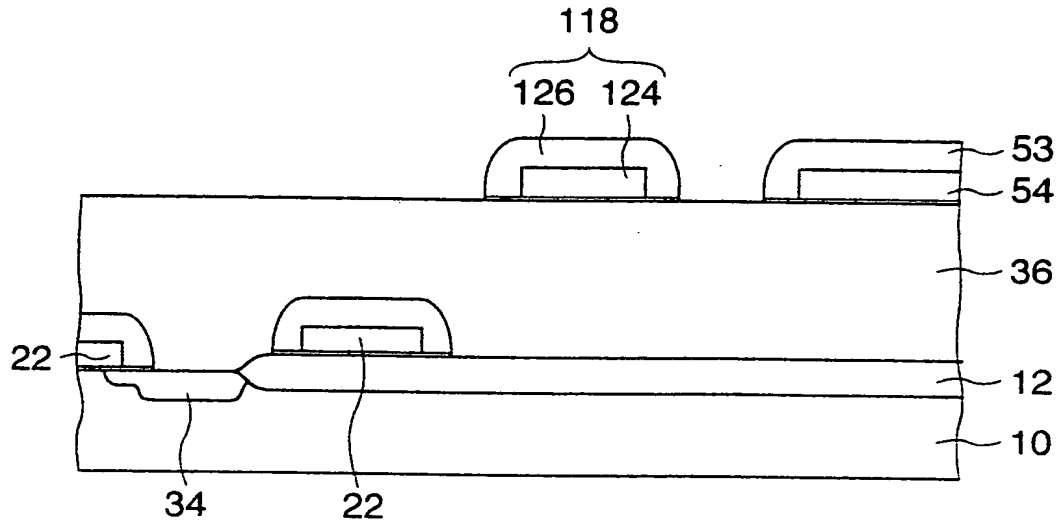
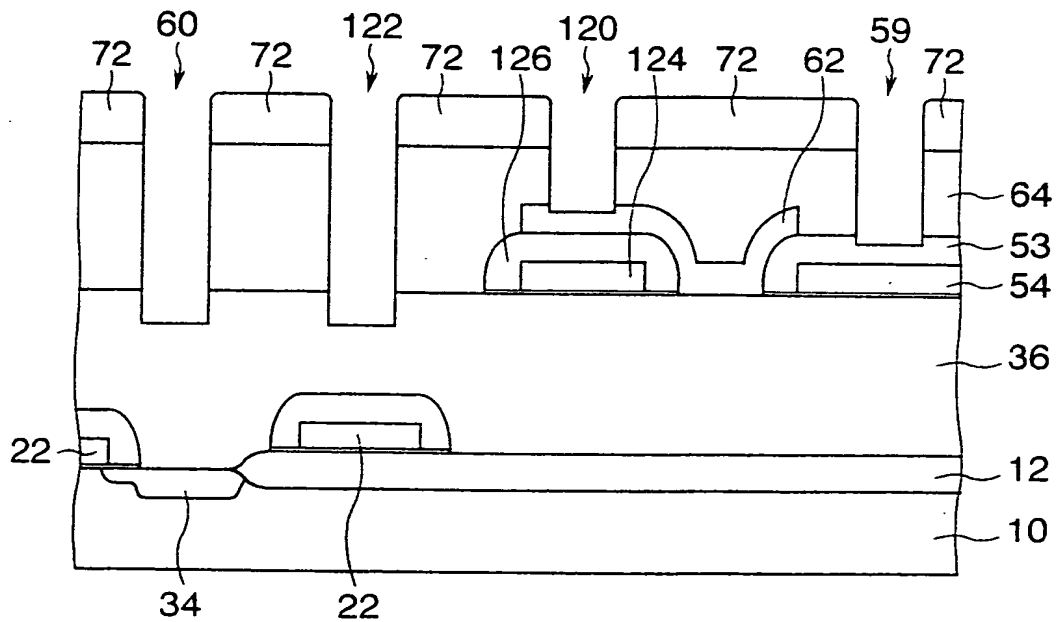
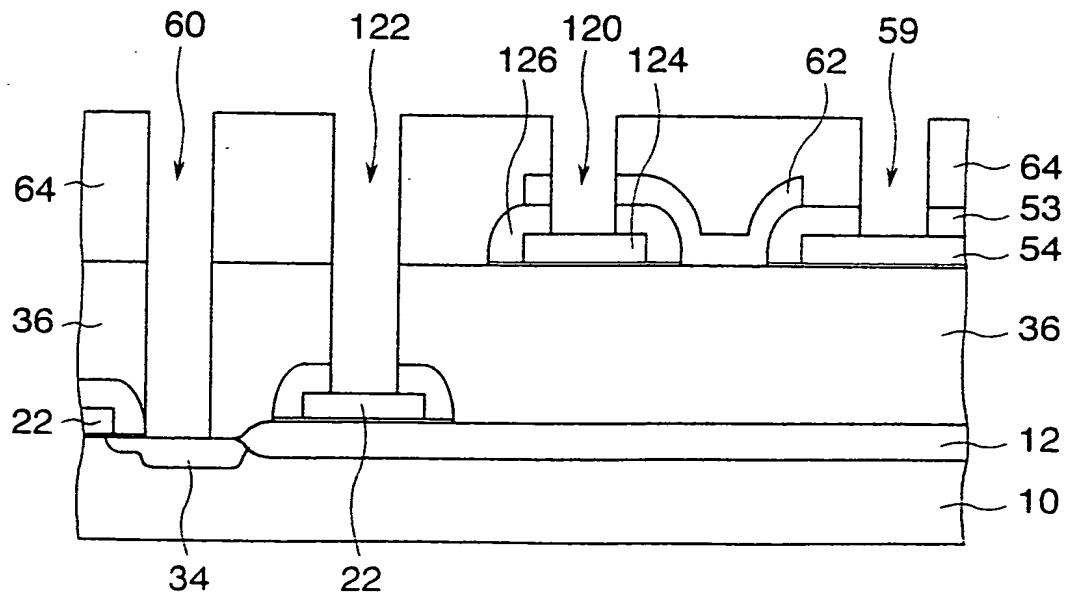


FIG.30B





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FIG.32A

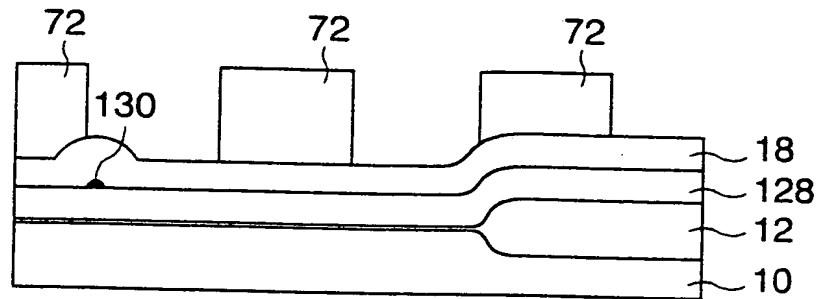


FIG.32B

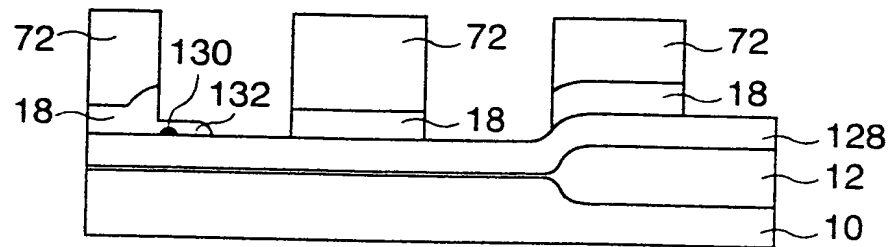


FIG.32C

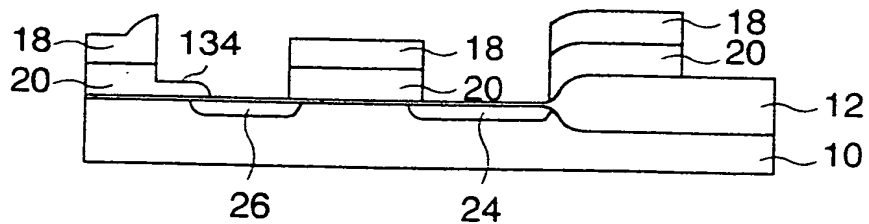
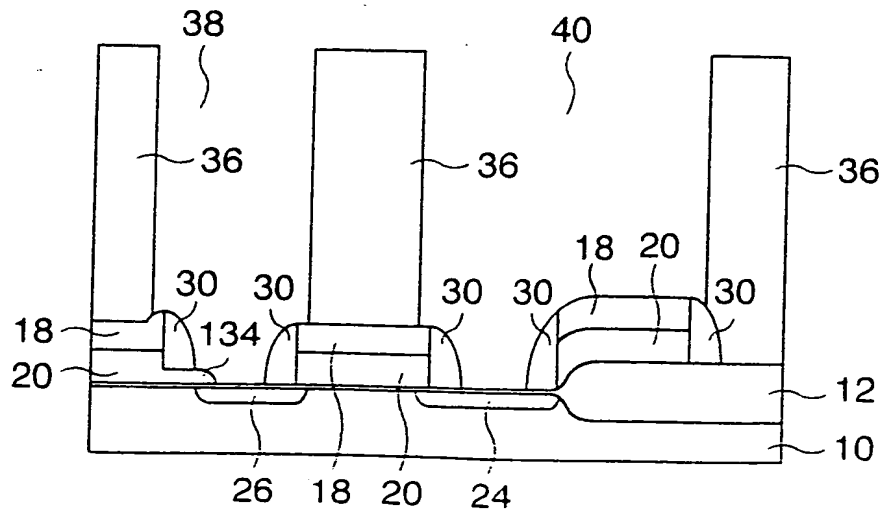


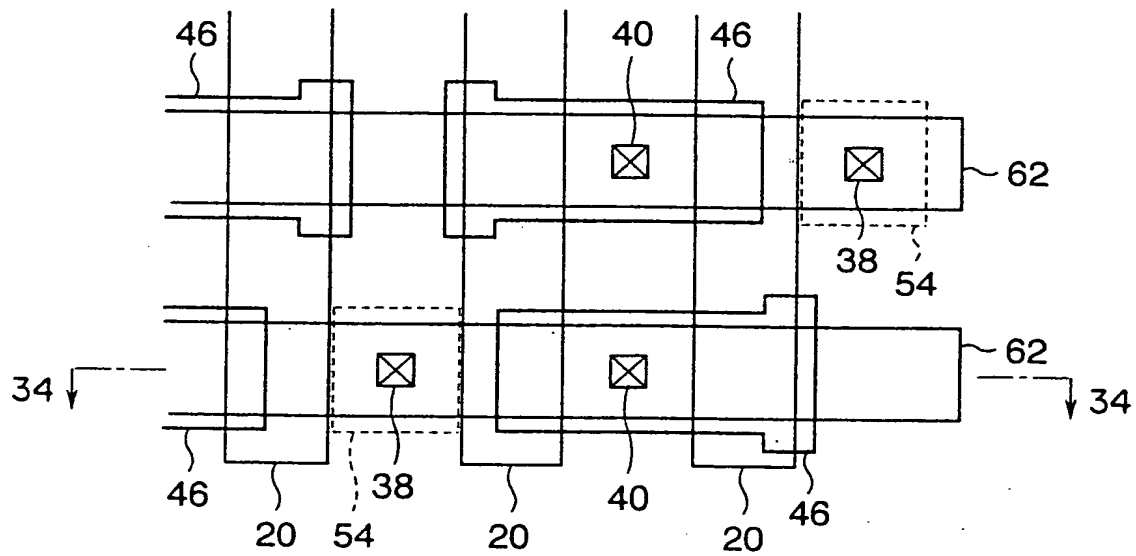
FIG.32D





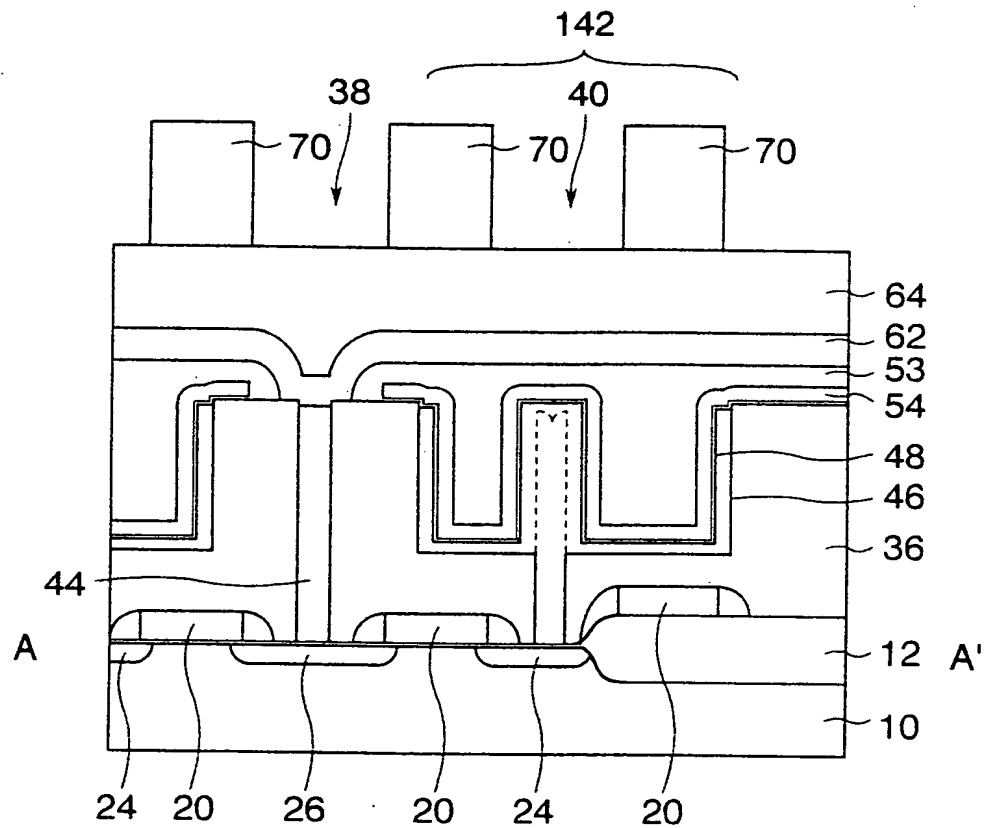
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FIG.33



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FIG.34



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FIG.35A

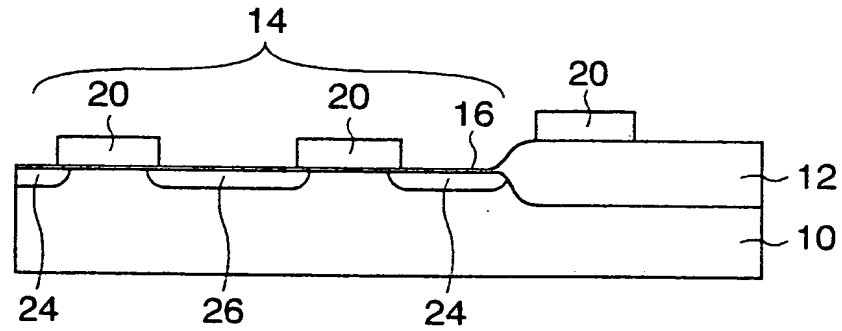


FIG.35B

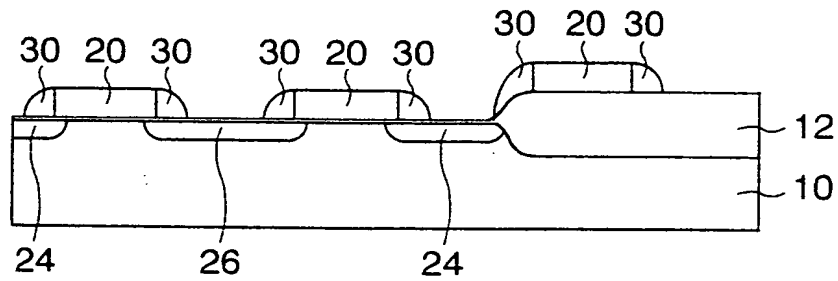
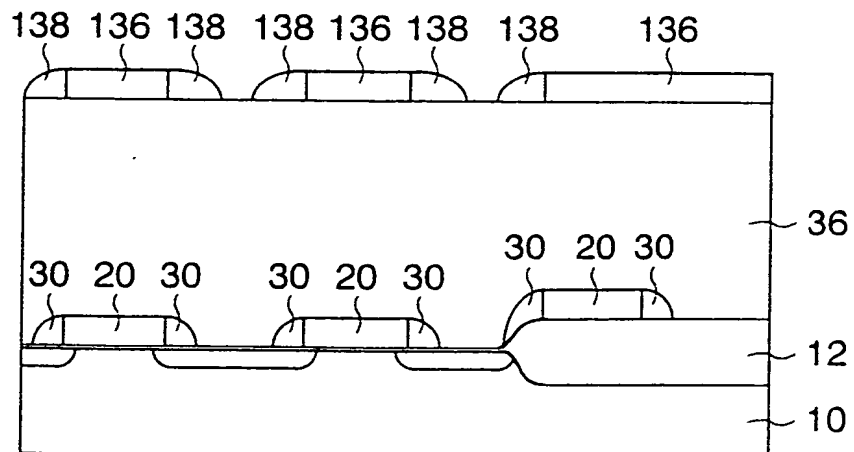


FIG.35C



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FIG.36A

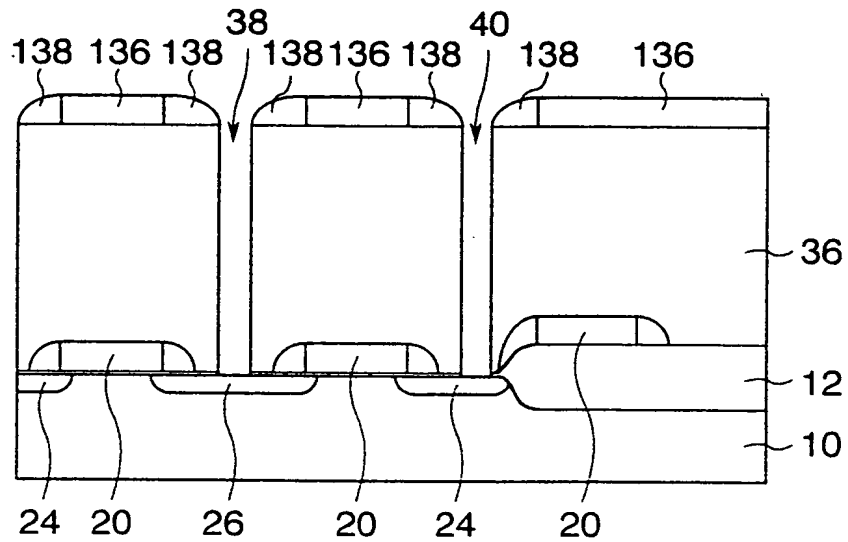
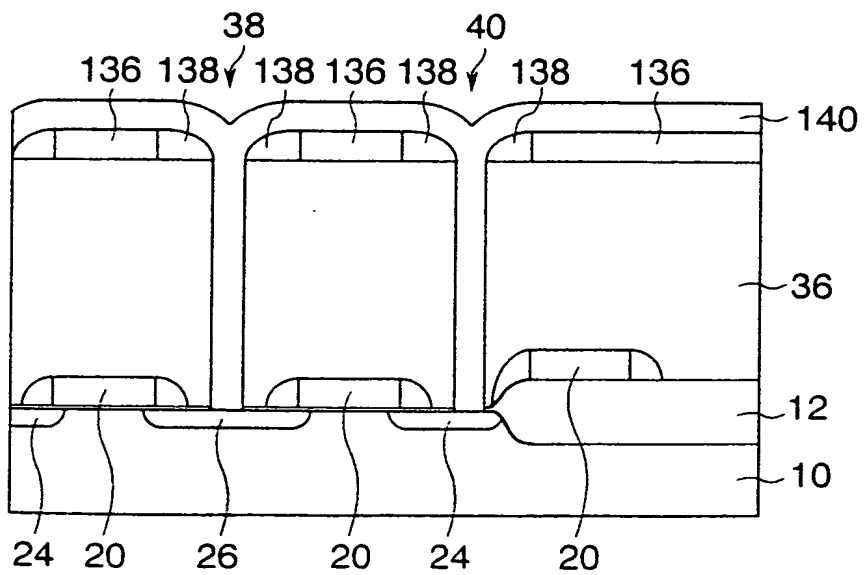


FIG.36B



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FIG.37A

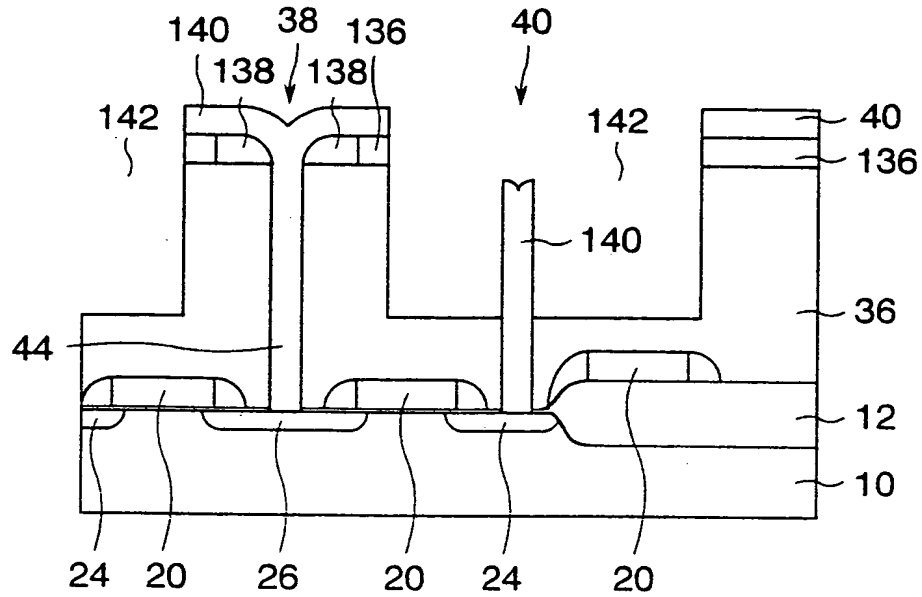
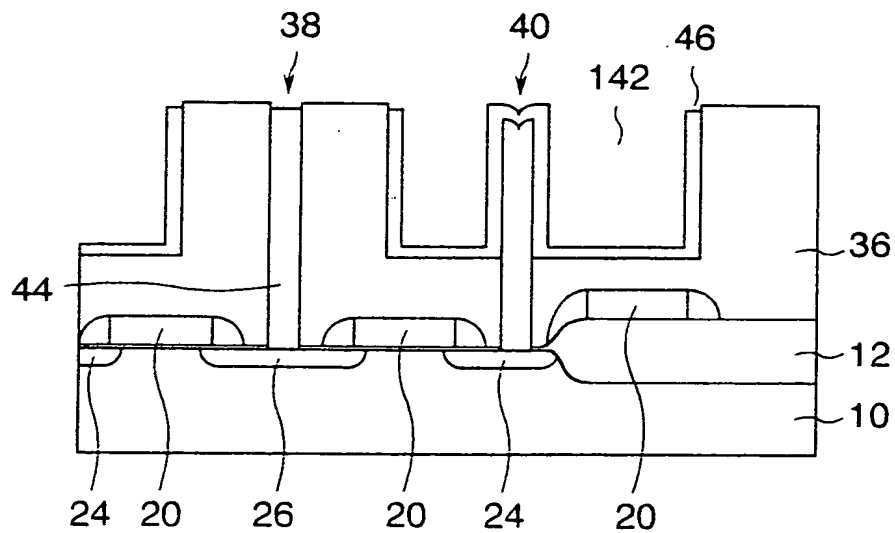
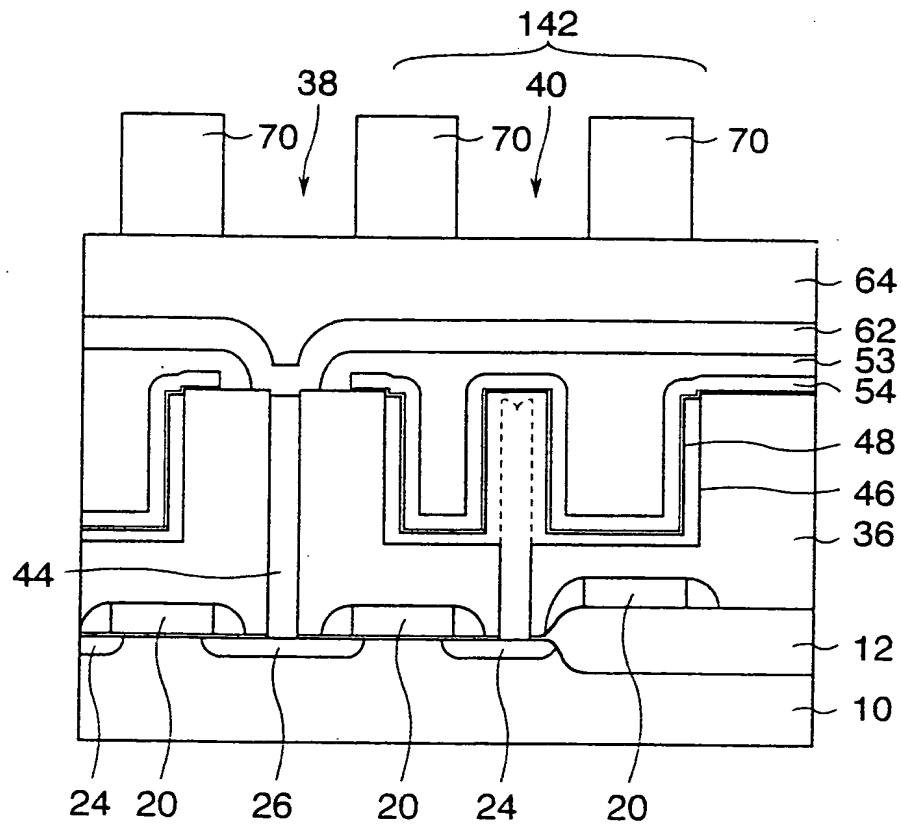


FIG.37B



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FIG.38



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FIG.39A

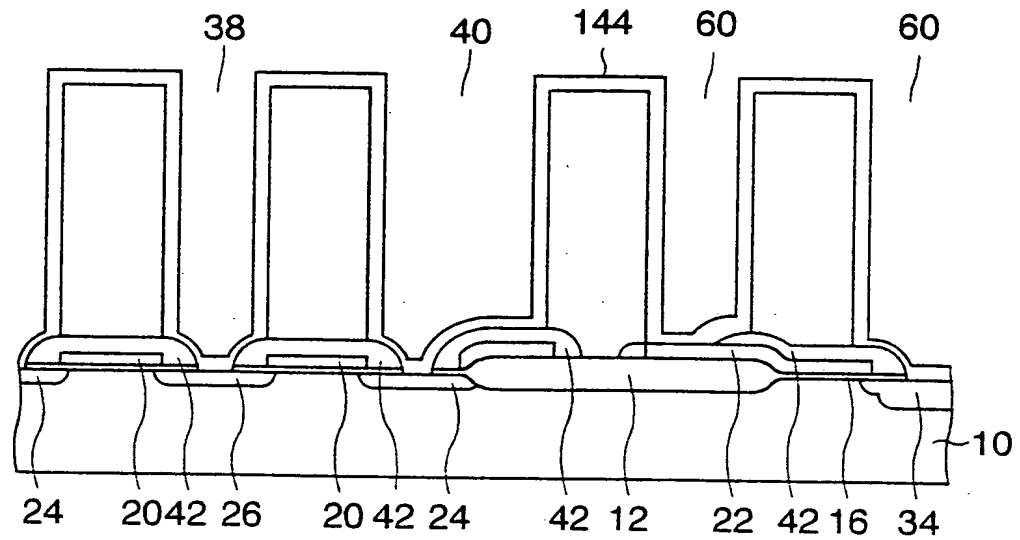
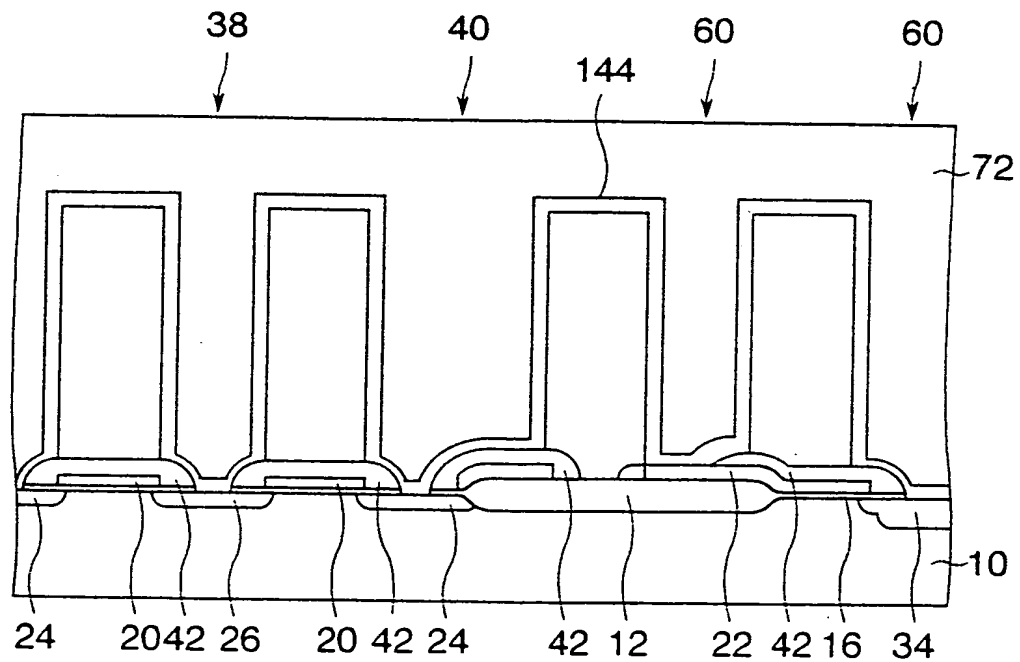


FIG. 39B



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FIG.40A

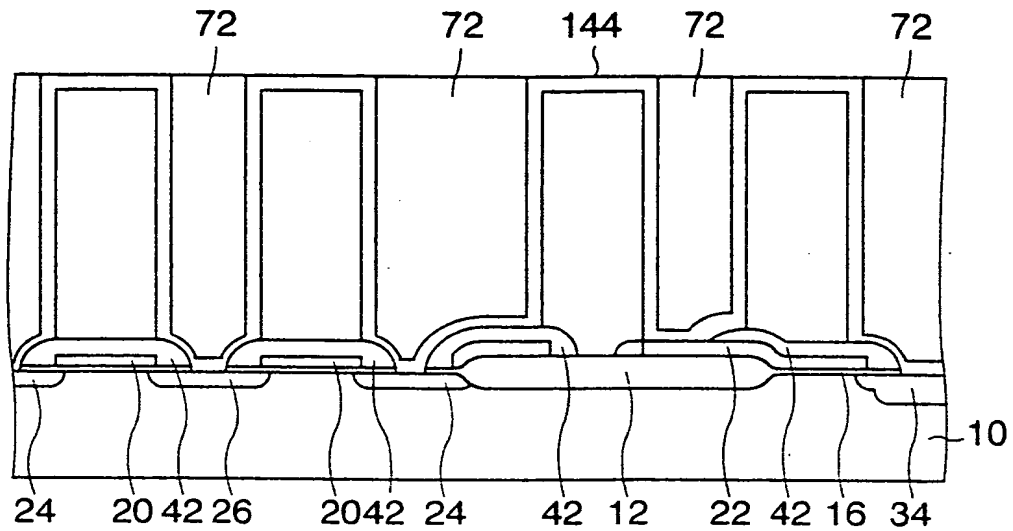
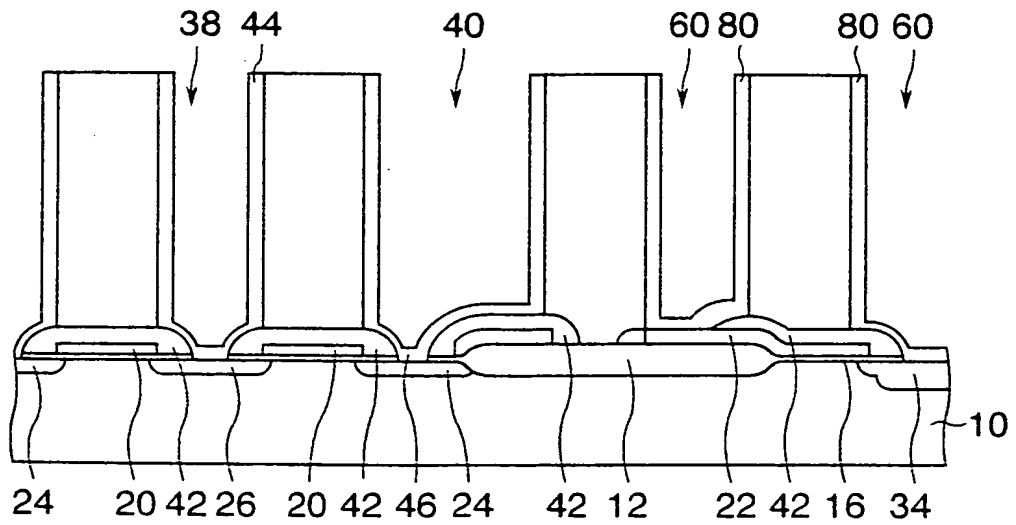


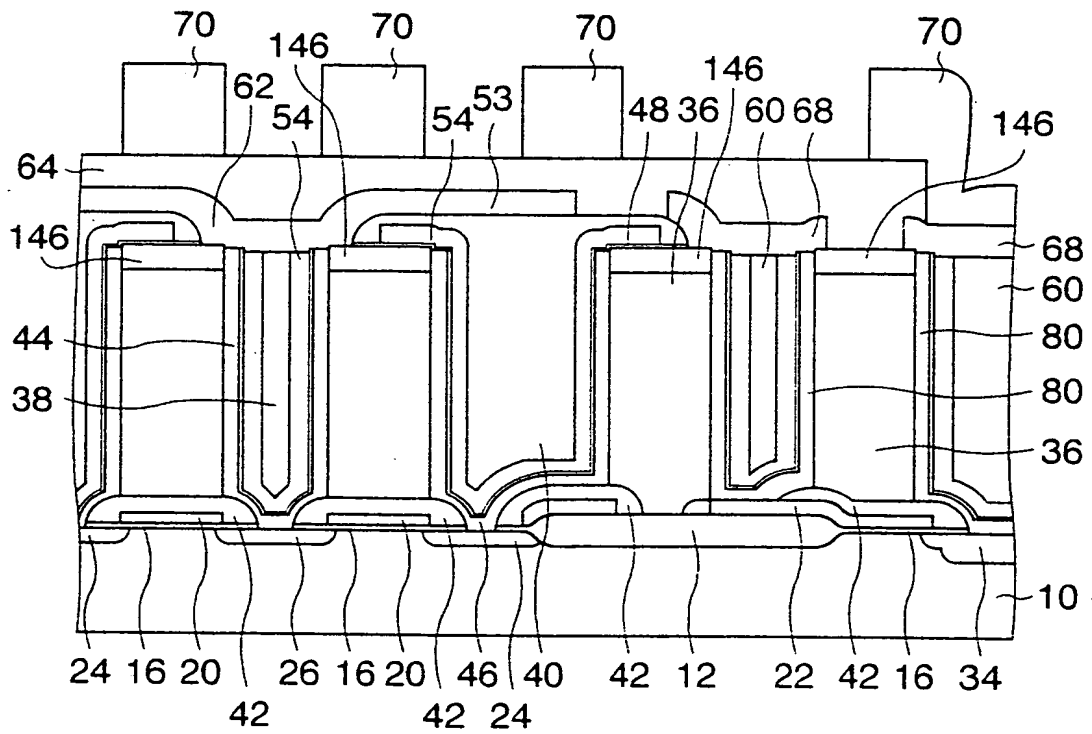
FIG.40B

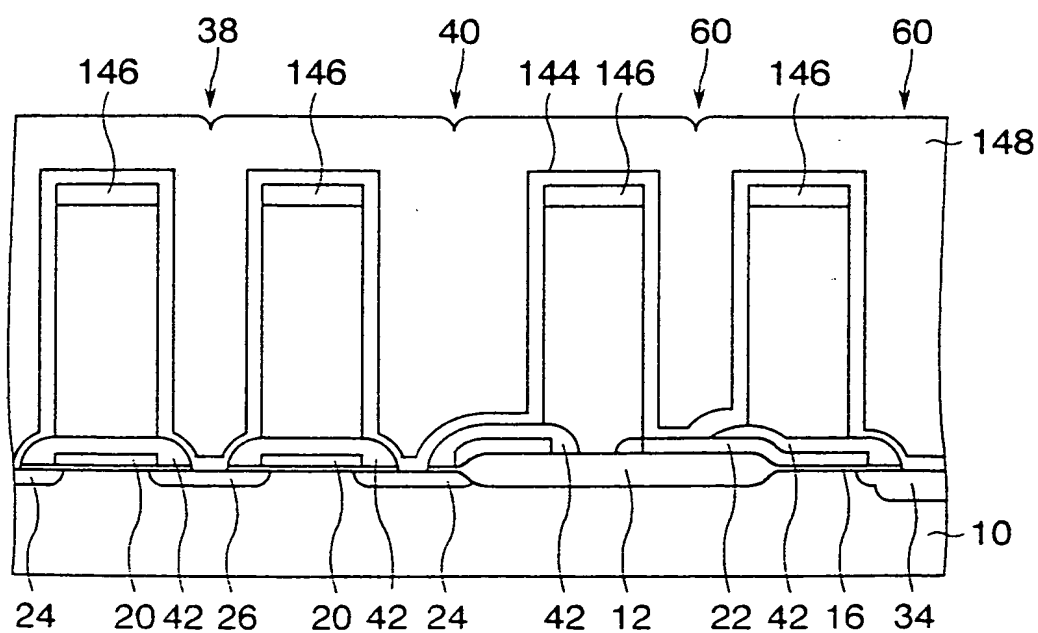




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FIG.41





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FIG.43A

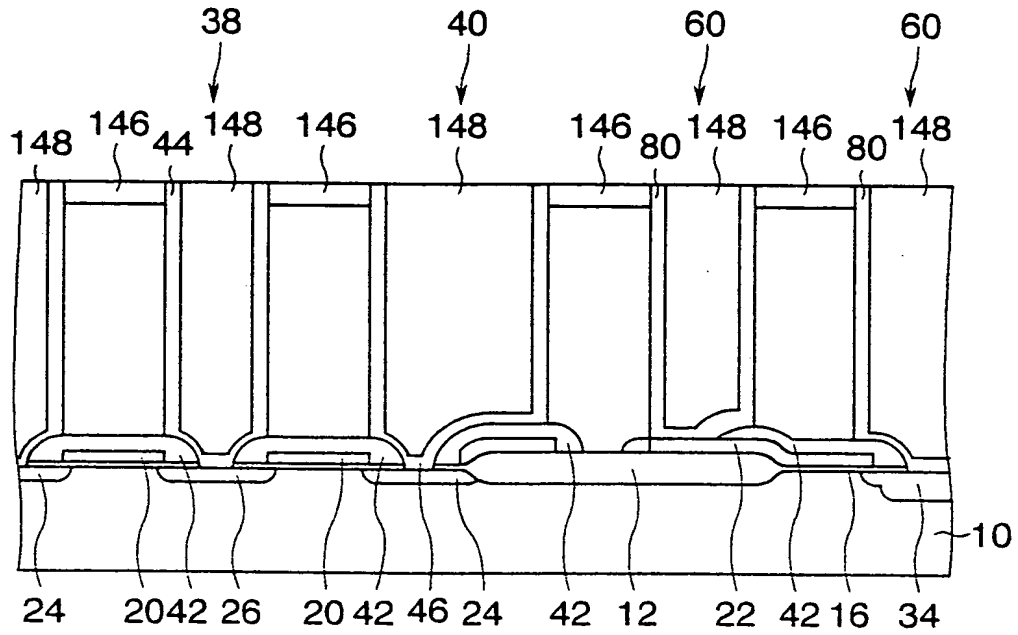
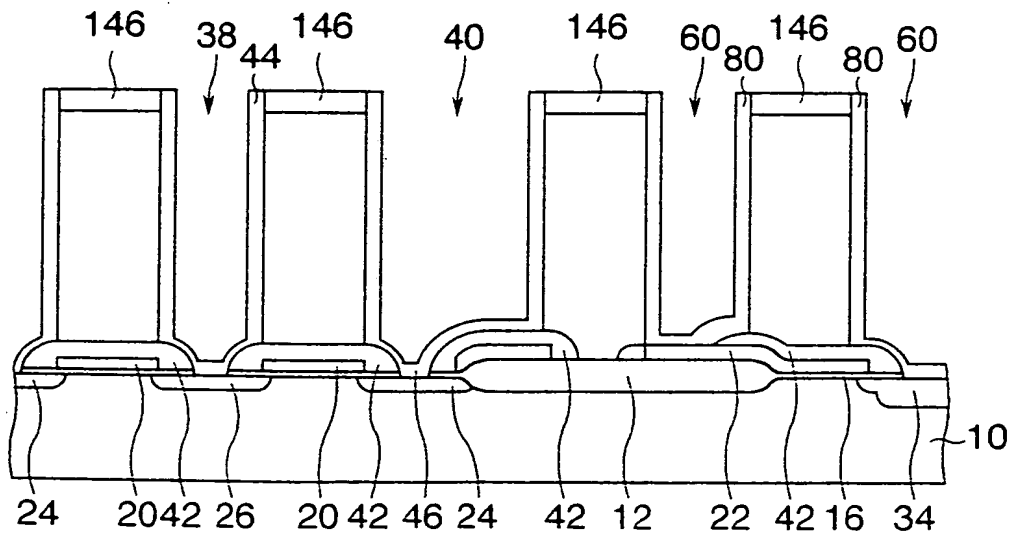
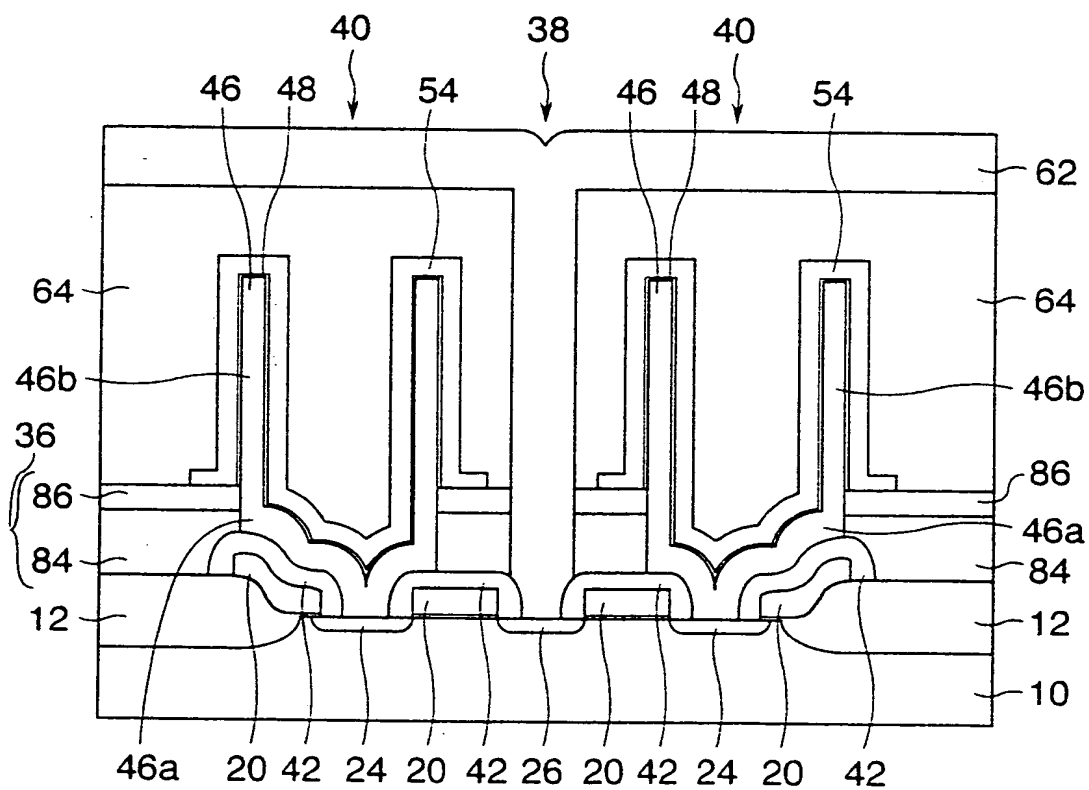


FIG.43B



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FIG. 44



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FIG.45A

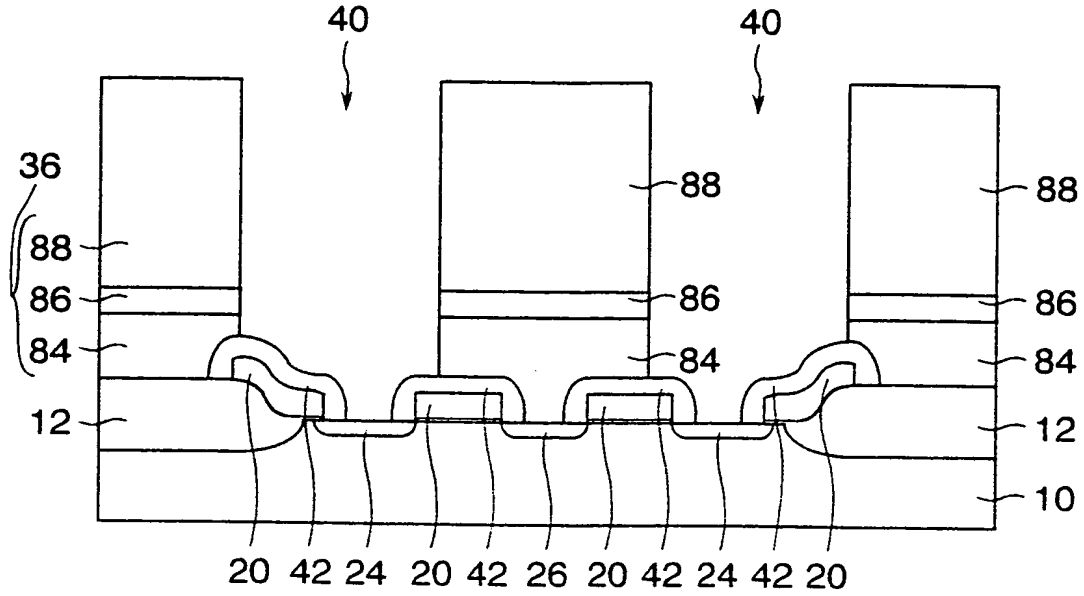
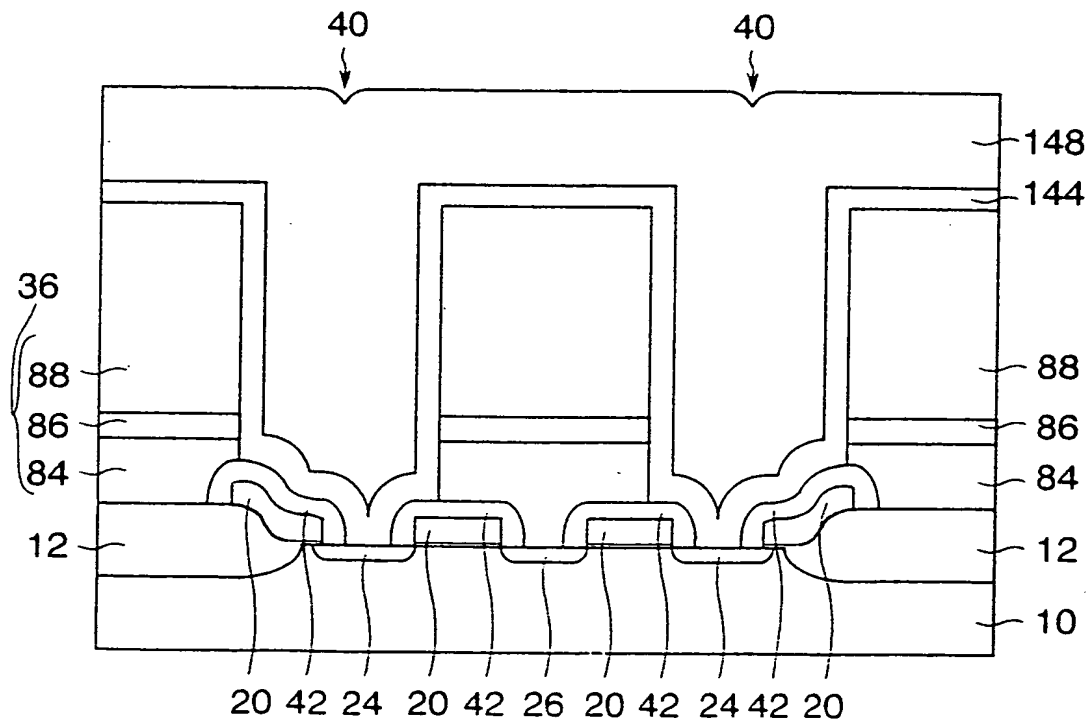


FIG.45B



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FIG.46A

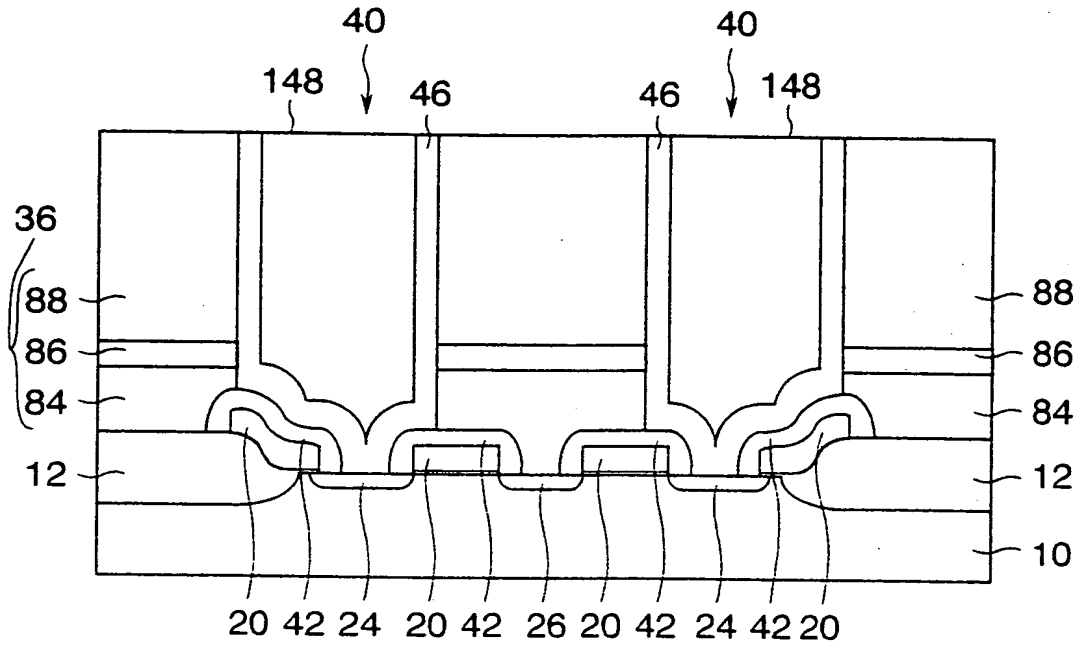
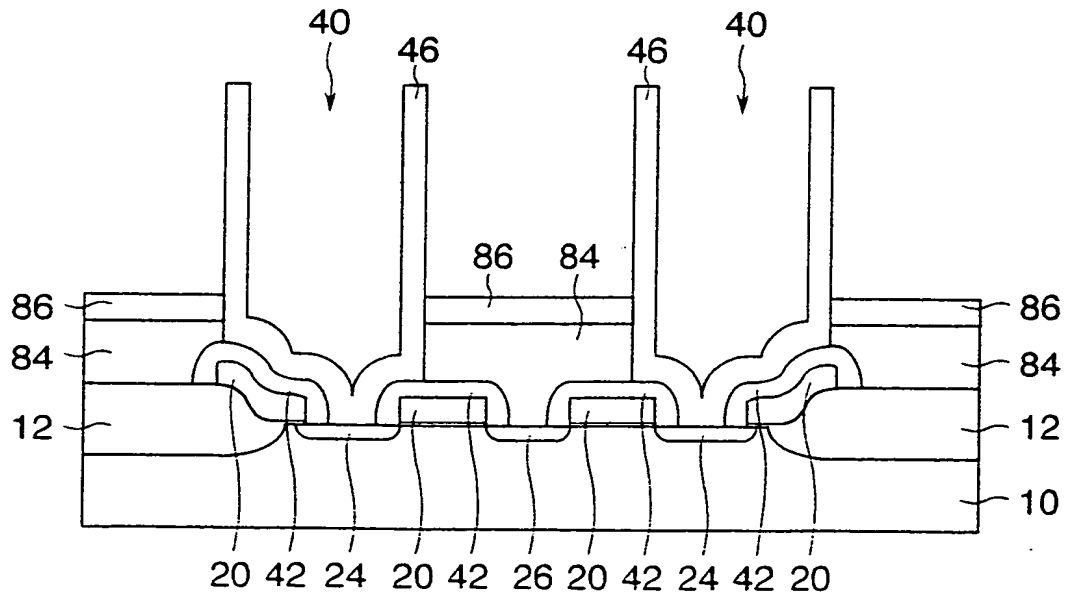


FIG.46B



[illegible]

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FIG.48A

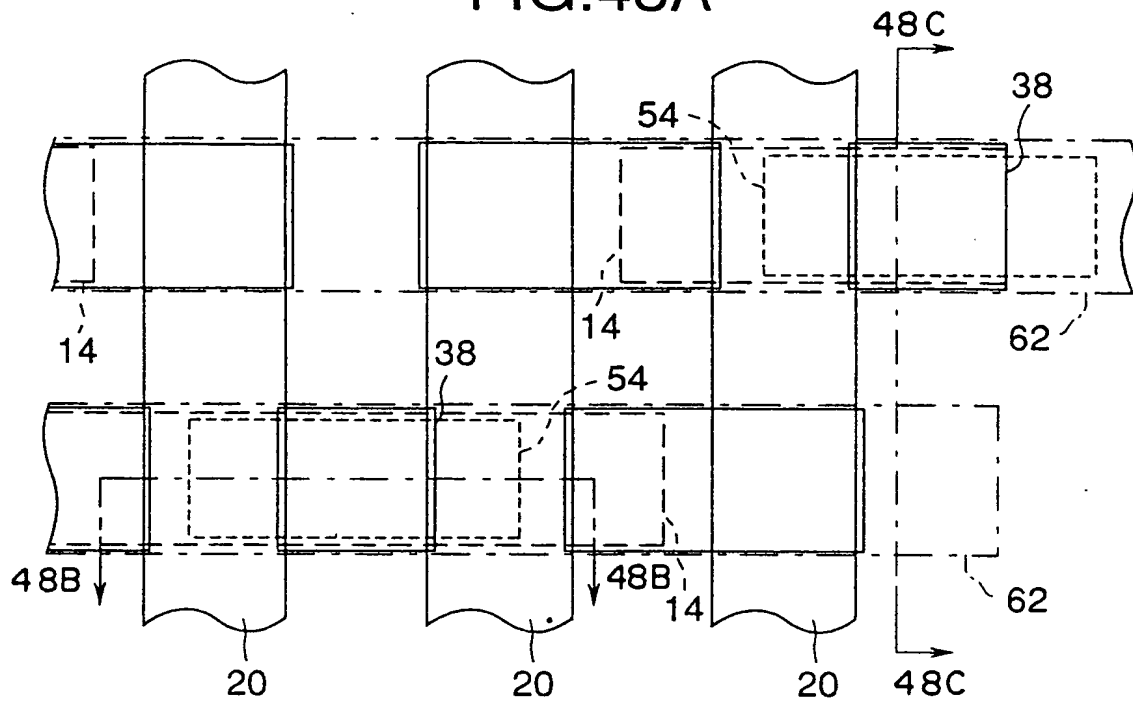


FIG.48B

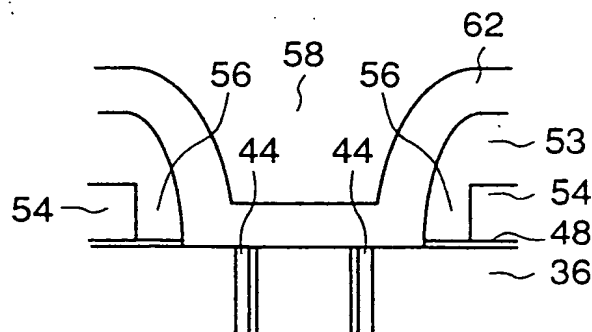
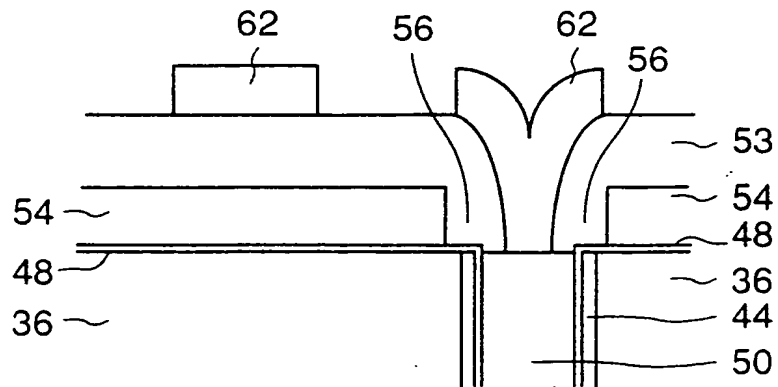


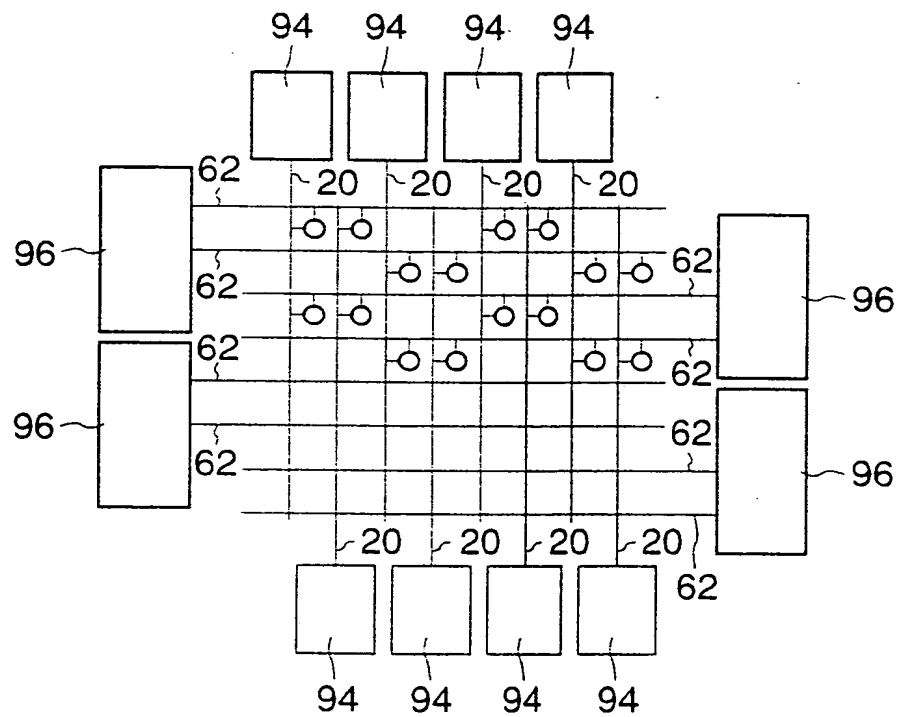
FIG.48C





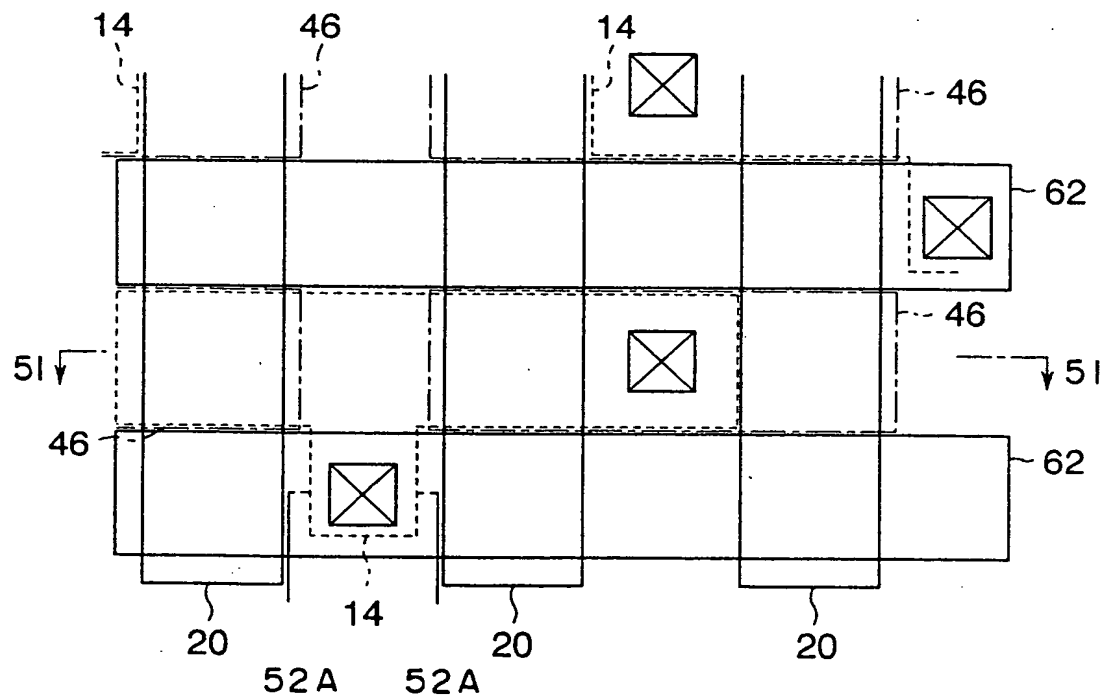
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FIG.49



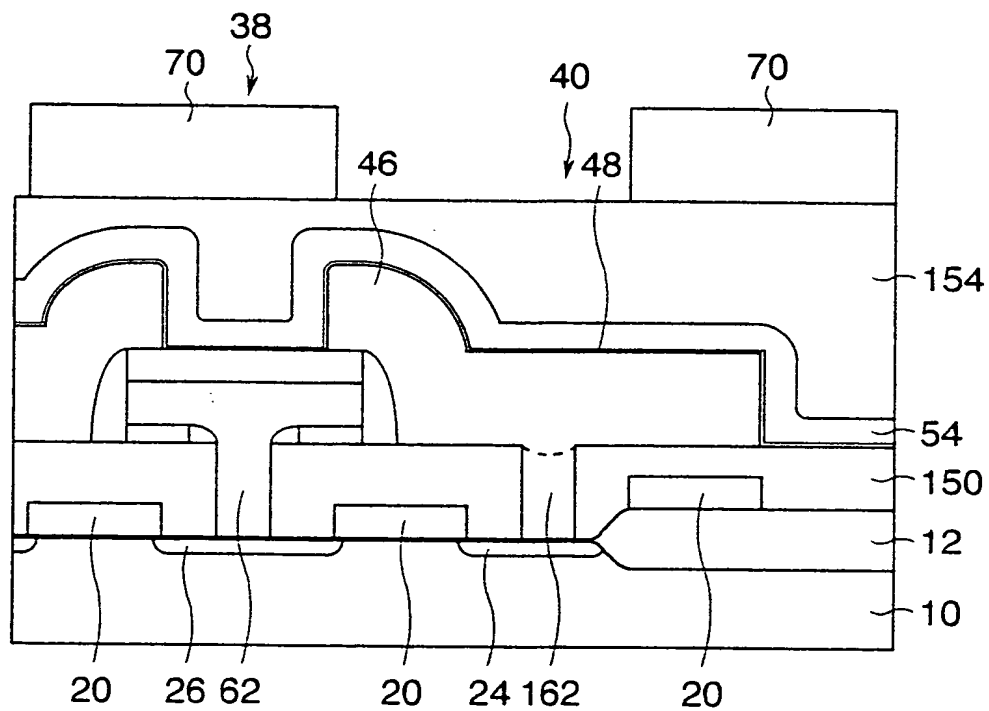
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FIG.50

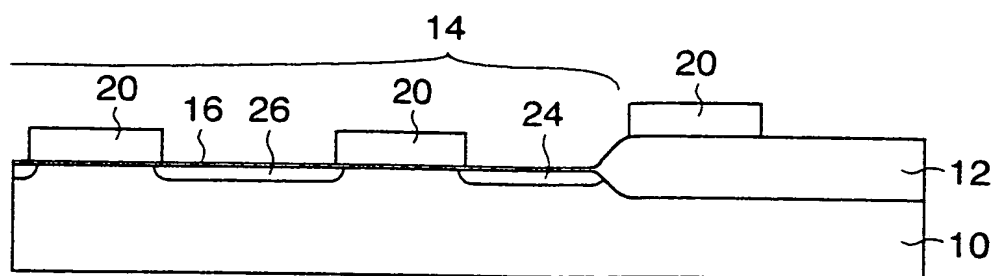


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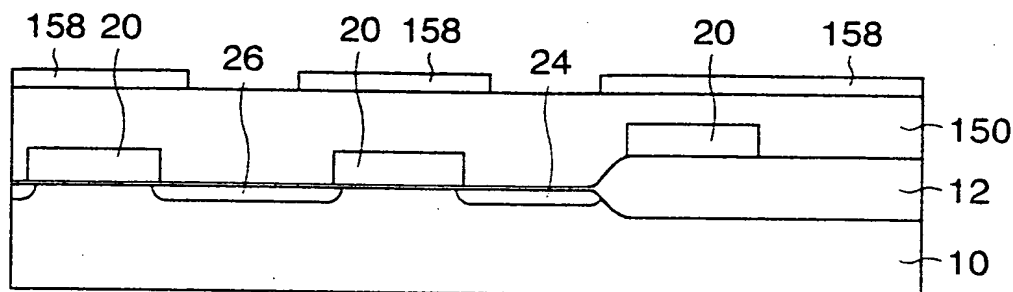
FIG.51



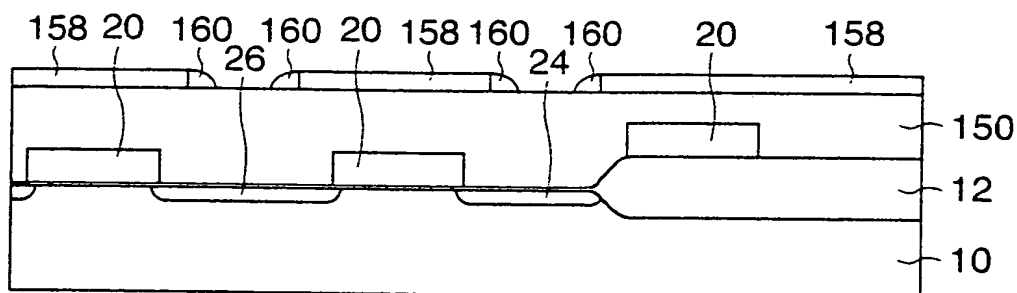
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**FIG.52A**



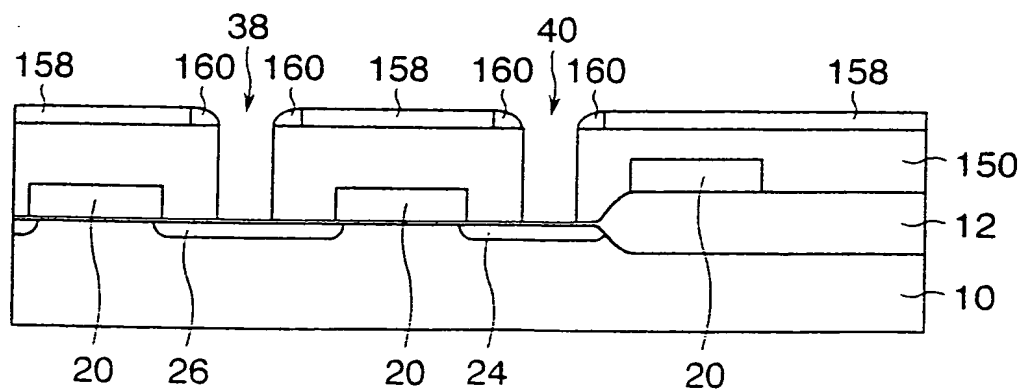
**FIG.52B**



**FIG.52C**



**FIG.52D**



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FIG.53A

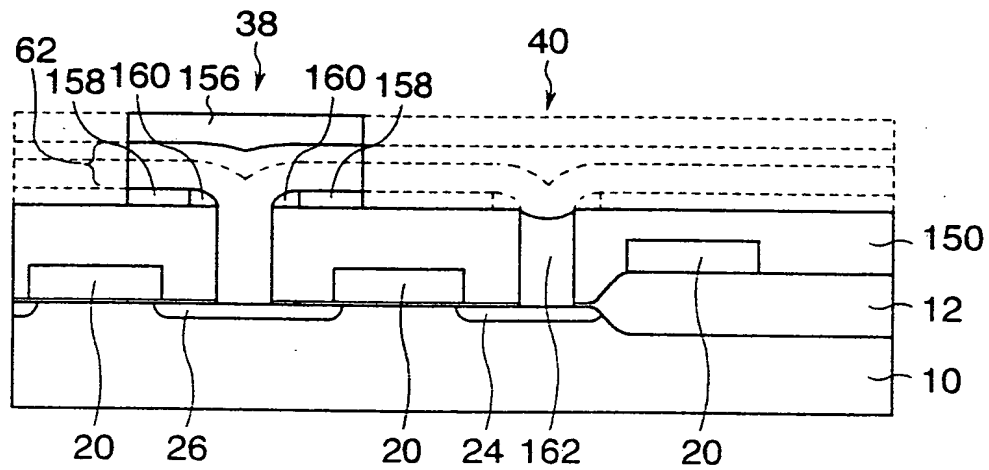
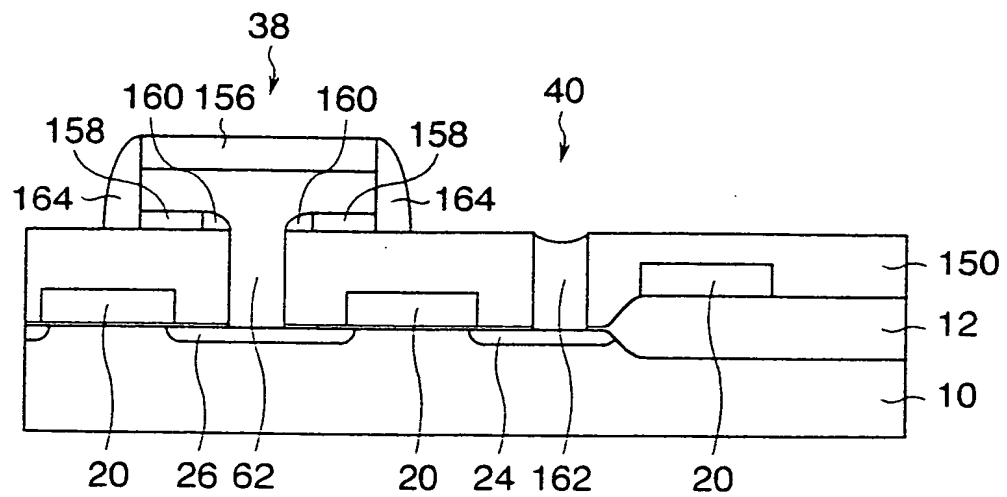


FIG.53B



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FIG.54A

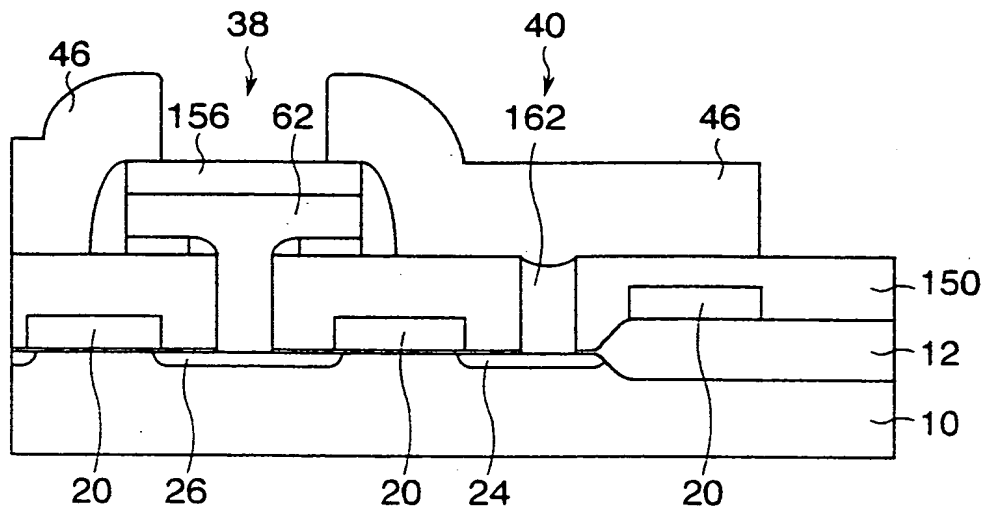
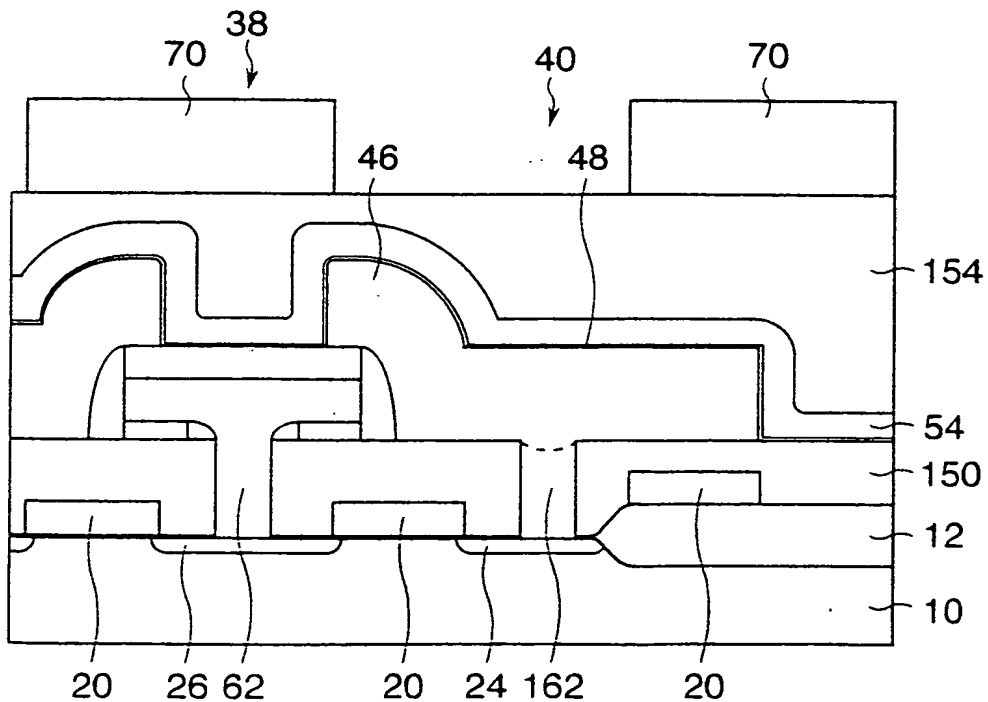
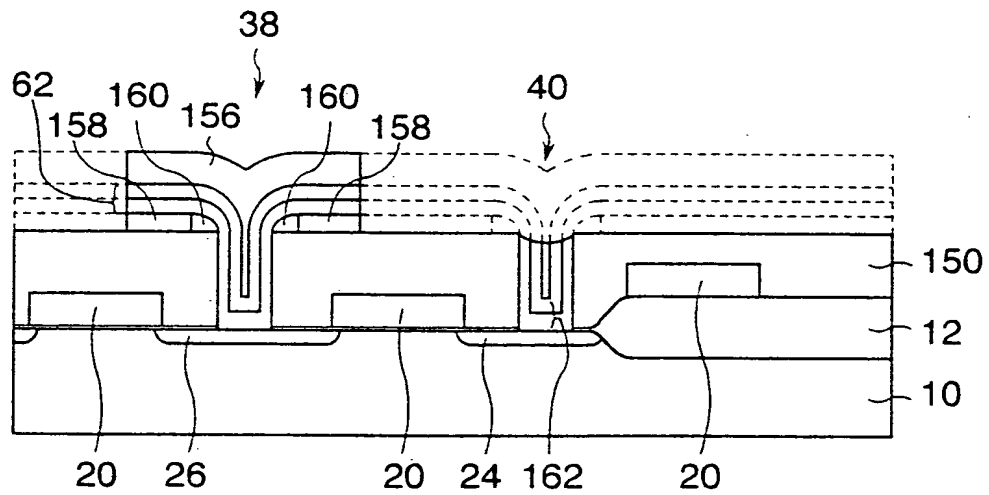


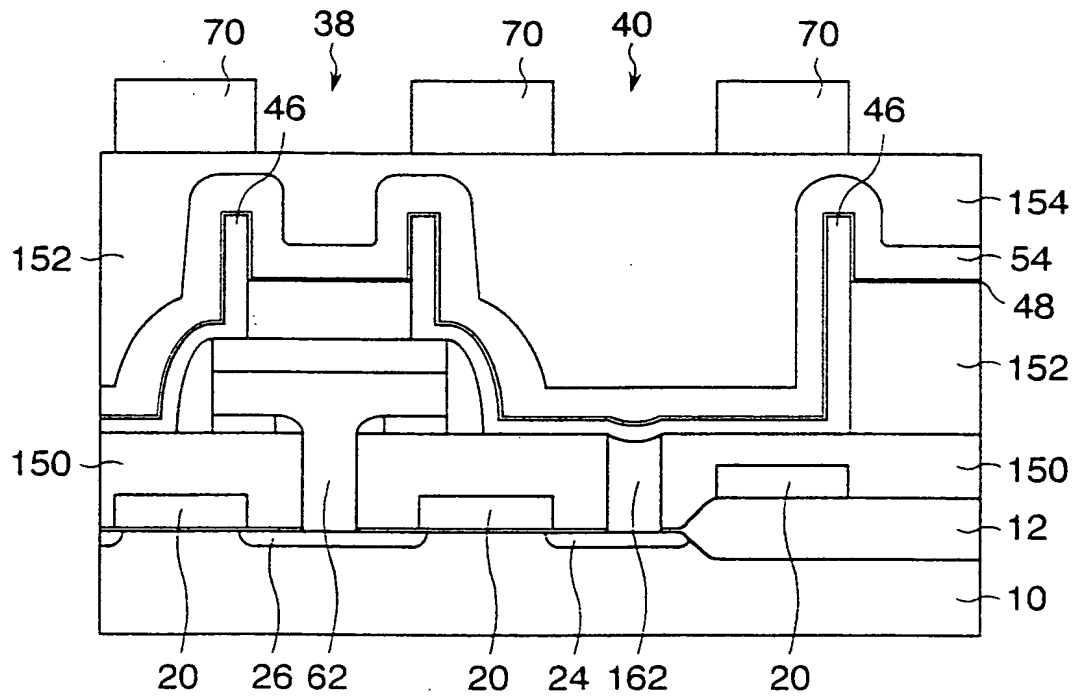
FIG.54B



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FIG.55







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FIG.57A

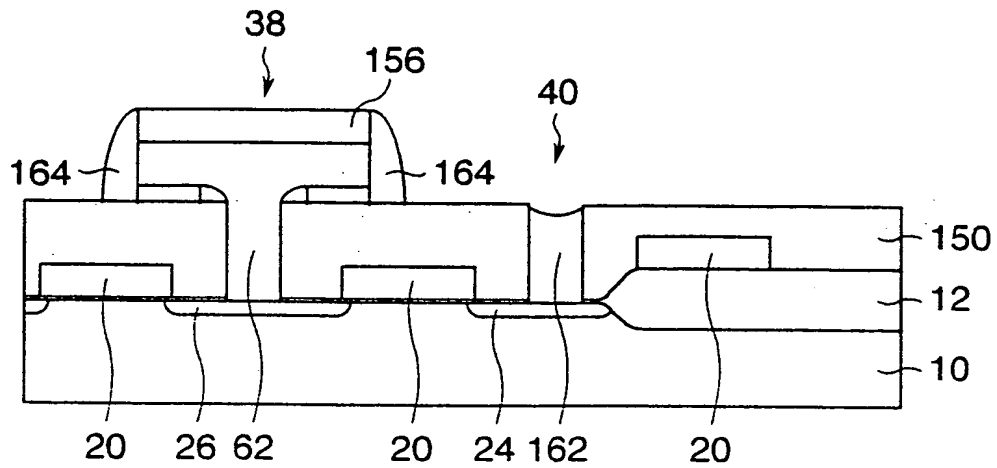
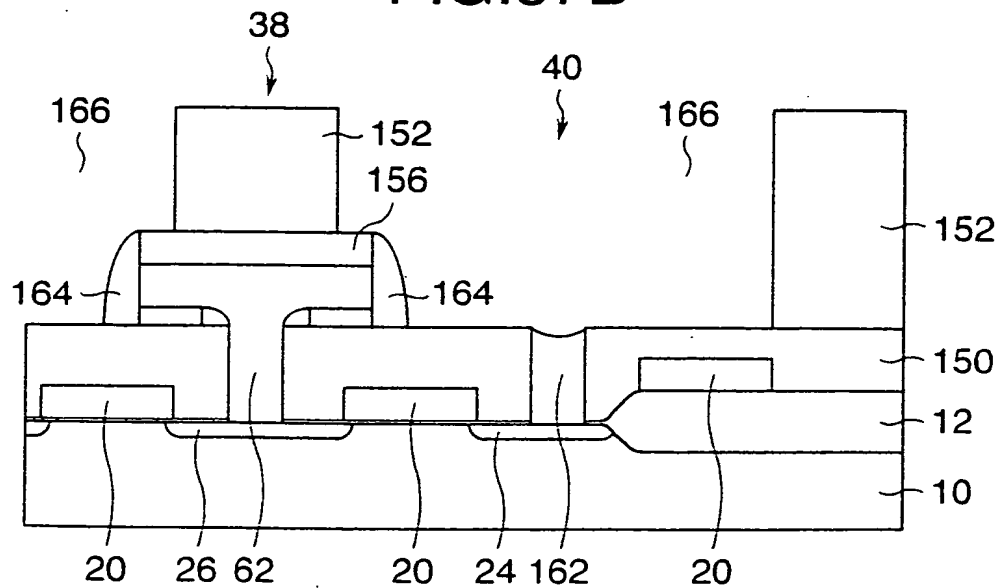


FIG.57B



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FIG.58A

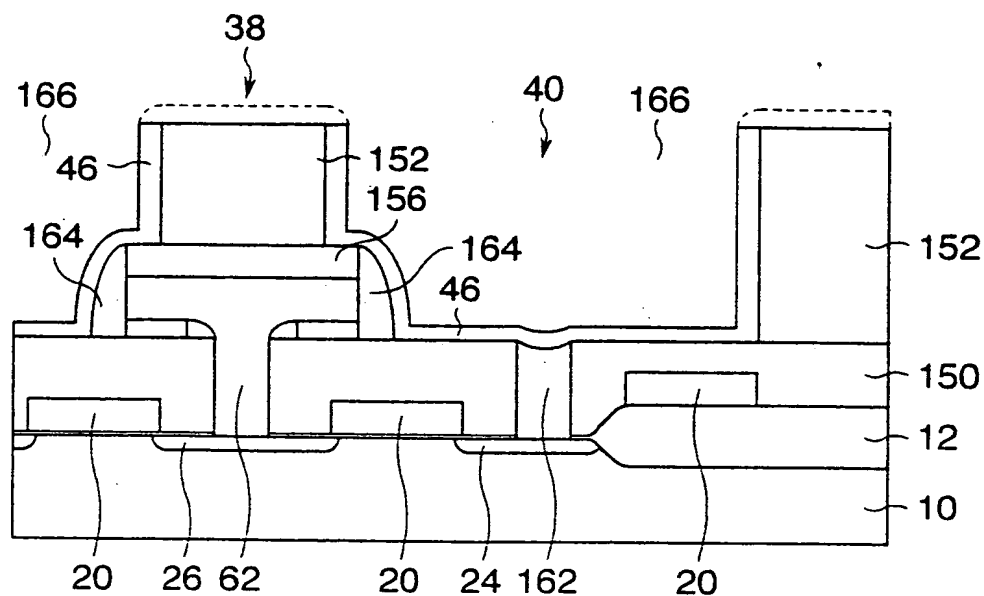
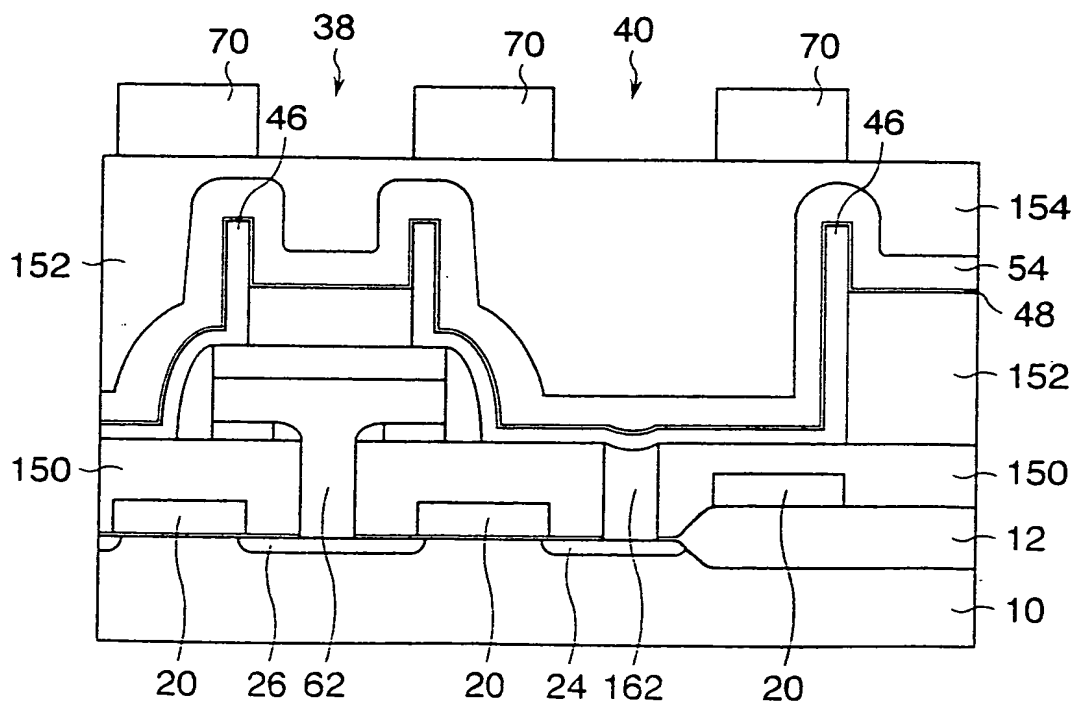
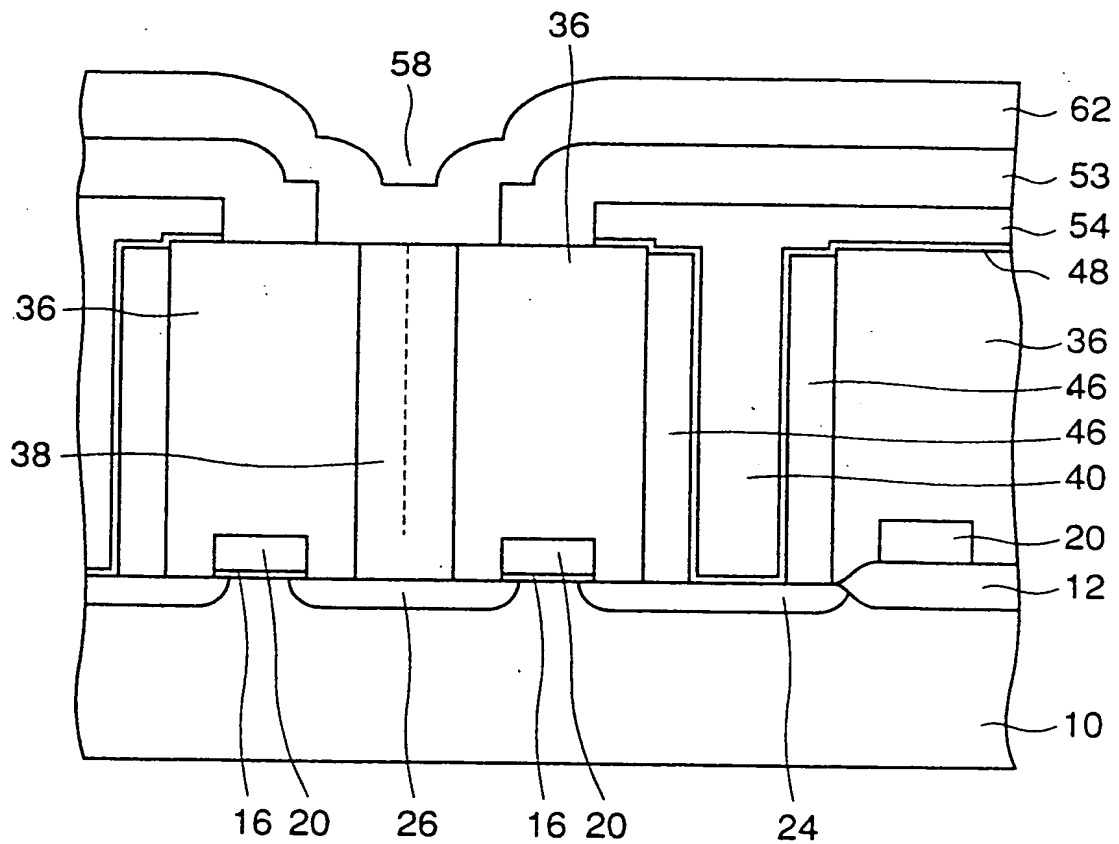


FIG. 58B



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**FIG.59**  
PRIOR ART



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FIG.60

PRIOR ART

